

STM32系列

产品选型手册



32位微控制器(MCU)
32位微处理器(MPU)



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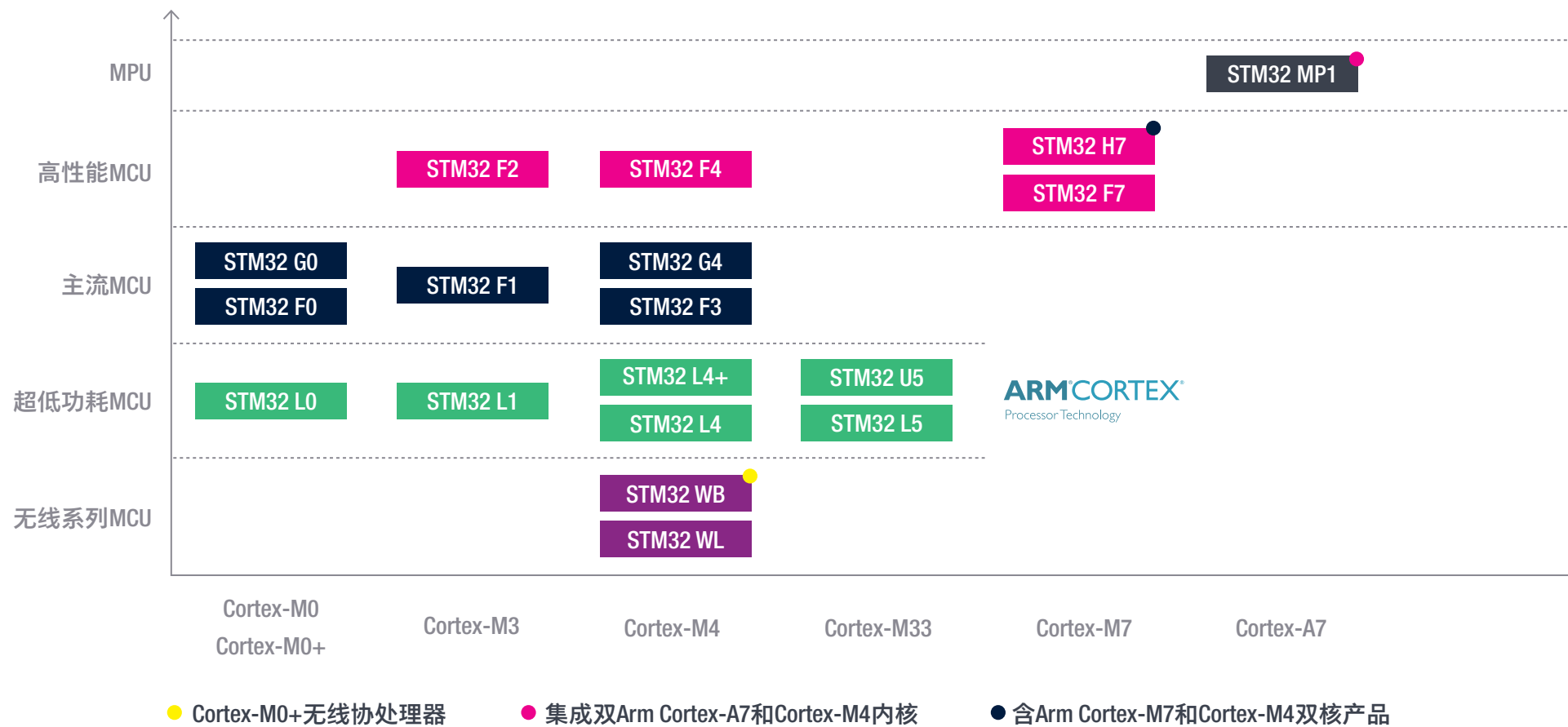
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基于Arm Cortex内核的32位MCU和MPU



STM32 G0系列 – Arm® Cortex®-M0+全新入门级MCU

[illegible]

STM32 G0系列 – Arm® Cortex®-M0+全新入门级MCU

[illegible]

STM32 G0系列 – Arm® Cortex®-M0+全新入门级MCU

Commercial Product Code	Core & Memory Specifications										Performance & Power Management										Connectivity & I/O										Advanced Features & Compliance																									
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	Edprom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb ADC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadsPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAT	SPDIFRX	DPSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC					
STM32G031C8U6	64	Cortex-M0+	64	8	0	UFQFPN48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32G041J6M6	64	Cortex-M0+	32	8	0	SO-8	6	1.7-3.6	5	1	1	2	0	1	8	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041F6P6	64	Cortex-M0+	32	8	0	TSSOP 20	18	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041F8P6	64	Cortex-M0+	64	8	0	TSSOP 20	18	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041Y8Y6	64	Cortex-M0+	64	8	0	WLCSP18	16	1.7-3.6	5	1	1	2	0	1	16	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0		
STM32G041G6U6	64	Cortex-M0+	32	8	0	UFQFPN 28	26	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0		
STM32G041G8U6	64	Cortex-M0+	64	8	0	UFQFPN 28	26	1.7-3.6	5	1	1	2	0	1	17	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041K6T6	64	Cortex-M0+	32	8	0	LQFP32	30	1.7-3.6	5	1	1	2	0	1	18	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041K8T6	64	Cortex-M0+	64	8	0	LQFP32	30	1.7-3.6	5	1	1	2	0	1	18	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041K8U6	64	Cortex-M0+	64	8	0	UFQFPN32	30	1.7-3.6	5	1	1	2	0	1	18	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041C6T6	64	Cortex-M0+	32	8	0	LQFP48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0		
STM32G041C8T6	64	Cortex-M0+	64	8	0	LQFP48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0			
STM32G041C8U6	64	Cortex-M0+	64	8	0	UFQFPN48	44	1.7-3.6	5	1	1	2	0	1	19	0	0	0	0	0	0	2	0	[1]	2	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0		
STM32G071EBY6	64	Cortex-M0+	128	36	0	WLCSP25	23	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32G071G8U6	64	Cortex-M0+	64	36	0	UFQFPN28	26	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32G071GBU6	64	Cortex-M0+	128	36	0	UFQFPN28	26	1.7-3.6	8	1	1	2	0	1	12	0	0	2	2	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32G071K8T6	64	Cortex-M0+	64	36	0	LQFP32	30	1.7-3.6	8	1	1	2	0	1	13	0	0	2	2	0	0	2	0	[1]	2	4	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 G0系列 – Arm® Cortex®-M0+全新入门级MCU

[illegible]

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadsPI/OctoSPI	I2S	I2C	U(S)ART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC						
STM32G4x1基本型 - 170 MHz																																																									
STM32G431K6T6	170 MHz	Cortex-M4	32	32	0	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	
STM32G431K8T6	170 MHz	Cortex-M4	64	32	0	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431KBT6	170 MHz	Cortex-M4	128	32	0	LQFP32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431K6U6	170 MHz	Cortex-M4	32	32	0	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431K8U6	170 MHz	Cortex-M4	64	32	0	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431KBU6	170 MHz	Cortex-M4	128	32	0	UFQFPN32	26	1.71-3.6	9	1	2	1	0	2	11	0	0	4	4	3	1	1	3	0	[2]	3	2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431C6T6	170 MHz	Cortex-M4	32	32	0	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431C8T6	170 MHz	Cortex-M4	64	32	0	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431CBT6	170 MHz	Cortex-M4	128	32	0	LQFP48	38	1.71-3.6	9	1	2	1	0	2	17	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431C6U6	170 MHz	Cortex-M4	32	32	0	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431C8U6	170 MHz	Cortex-M4	64	32	0	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431CBU6	170 MHz	Cortex-M4	128	32	0	UFQFPN48	42	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431CBY6	170 MHz	Cortex-M4	128	32	0	WLCSP49	41	1.71-3.6	9	1	2	1	0	2	18	0	0	4	4	3	1	1	3	0	[2]	3	3	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431R6T6	170 MHz	Cortex-M4	32	32	0	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431R8T6	170 MHz	Cortex-M4	64	32	0	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431RB6T6	170 MHz	Cortex-M4	128	32	0	LQFP64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
STM32G431R6U6	170 MHz	Cortex-M4	32	32	0	UFBGA64	52	1.71-3.6	9	1	2	1	0	2	23	0	0	4	4	3	1	1	3	0	[2]	3	3+1	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

[illegible]

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

[illegible]

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

Commercial Product Code	STM32G4x3 High Performance - 170 MHz																																																							
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMCC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
	170 MHz	Cortex-M4	512	112	0	UFBGA64	52	1.71-3.6	10	1	3	1	0	3	24	0	0	4	4	4	1	1	3	1 [QUAD]	[2]	3	3+2	1	2 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1	0
	170 MHz	Cortex-M4	512	112	0	WLCSP64	52	1.71-3.6	10	1	3	1	0	3	24	0	0	4	4	4	1	1	3	1 [QUAD]	[2]	3	3+2	1	2 [FD]	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	1	0	
STM32G4A1MET6	170 MHz	Cortex-M4	512	112	0	LQFP80	66	1.71-3.6	10	1	3	1	0	3	32	0	0	4	4	4	1	1	3	1 [QUAD]	[2]	3	3+2	1	2 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	1	0		
STM32G4A1VET6	170 MHz	Cortex-M4	512	112	0	LQFP100	86	1.71-3.6	10	1	3	1	0	3	36	0	0	4	4	4	1	1	3	1 [QUAD]	[2]	3	3+2	1	2 [FD]	0	0	0	1	0	0	0	1	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	1	0		
STM32G473CBT6	170 MHz	Cortex-M4	128	120	0	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0		
STM32G473CCT6	170 MHz	Cortex-M4	256	120	0	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473CET6	170 MHz	Cortex-M4	512	120	0	LQFP48	38	1.71-3.6	10	2	3	1	0	5	20	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473CBU6	170 MHz	Cortex-M4	128	120	0	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473CCU6	170 MHz	Cortex-M4	256	120	0	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473CEU6	170 MHz	Cortex-M4	512	120	0	UFQFPN48	42	1.71-3.6	10	2	3	1	0	5	21	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473RBT6	170 MHz	Cortex-M4	128	120	0	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473RCT6	170 MHz	Cortex-M4	256	120	0	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473RET6	170 MHz	Cortex-M4	512	120	0	LQFP64	52	1.71-3.6	10	2	3	1	0	5	26	0	0	7	7	6	1	1	3	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473VBT6	170 MHz	Cortex-M4	128	120	0	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0				
STM32G473VCT6	170 MHz	Cortex-M4	256	120	0	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0			
STM32G473VET6	170 MHz	Cortex-M4	512	120	0	LQFP100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0				
STM32G473VBH6	170 MHz	Cortex-M4	128	120	0	TFBGA100	86	1.71-3.6	10	2	3	1	0	5	42	0	0	7	7	6	1	1	4	1 [QUAD]	[2]	4	3+2	1	3 [FD]	0	1	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	0				

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

[illegible]

STM32 G4系列 – Arm® Cortex®-M4模数混合型MCU

[illegible]

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

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STM32 F0系列 – Arm® Cortex®-M0入门级MCU

Commercial Product Code	Core Processor & Memory										Peripheral & Interface										System & Security																																					
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	EEProm (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadsPI/OctoSPI	I2C	I2S	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC								
STM32F031F6P6	48	Cortex-M0	32	4	0	TSSOP20	15	2-3.6	5	1	1	0	0	0	0	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0							
STM32F031E6Y6	48	Cortex-M0	32	4	0	WLCSP25	20	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32F031G6U6	48	Cortex-M0	32	4	0	UFQFPN28	23	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32F031K6T6	48	Cortex-M0	32	4	0	LQFP32	25	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F031K6U6	48	Cortex-M0	32	4	0	UFQFPN32	27	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F031C6T6	48	Cortex-M0	32	4	0	LQFP48	39	2-3.6	5	1	1	0	0	1	10	0	0	0	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F051K4T6	48	Cortex-M0	16	8	0	LQFP32	25	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F051K4U6	48	Cortex-M0	16	8	0	UFQFPN32	27	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	1	0	[1]	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F051K6T6	48	Cortex-M0	32	8	0	LQFP32	25	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F051K6U6	48	Cortex-M0	32	8	0	UFQFPN32	27	2-3.6	7	1	1	0	0	1	10	0	0	1	2	0	0	1	0	[1]	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32F051K8T6	48	Cortex-M0	64	8	0	LQFP32																																																				

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

[illegible]

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

[illegible]

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

[illegible]

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

[illegible]

STM32 F0系列 – Arm® Cortex®-M0入门级MCU

[illegible]

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IO Nb	Supply voltage	Nb Timer (16-bits)	Nb timer control Timer (16bits)	Nb High Resolution Timer	Nb Low Power Timer	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 12-bit Cell	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32F100超值型 - 24 MHz																																																						
STM32F100C4T6	24	Cortex-M3	16	4	0	LQFP48	37	2-3.6	5	0	1	0	0	1	10	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100C6T6	24	Cortex-M3	32	4	0	LQFP48	37	2-3.6	5	0	1	0	0	1	10	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100C8T6	24	Cortex-M3	64	8	0	LQFP48	37	2-3.6	6	0	1	0	0	1	10	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100CBT6	24	Cortex-M3	128	8	0	LQFP48	37	2-3.6	6	0	1	0	0	1	10	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100R4T6	24	Cortex-M3	16	4	0	LQFP64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100R4H6	24	Cortex-M3	16	4	0	TFBGA64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100R6T6	24	Cortex-M3	32	4	0	LQFP64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100R6H6	24	Cortex-M3	32	4	0	TFBGA64	51	2-3.6	5	0	1	0	0	1	16	0	0	2	0	0	0	0	1	0	0	1	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100R8T6	24	Cortex-M3	64	8	0	LQFP64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F100R8H6	24	Cortex-M3	64	8	0	TFBGA64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F100RBT6	24	Cortex-M3	128	8	0	LQFP64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F100RBH6	24	Cortex-M3	128	8	0	TFBGA64	51	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F100V8T6	24	Cortex-M3	64	8	0	LQFP100	80	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F100VBT6	24	Cortex-M3	128	8	0	LQFP100	80	2-3.6	6	0	1	0	0	1	16	0	0	2	0	0	0	0	2	0	0	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F100RCT6	24	Cortex-M3	256	24	0	LQFP64	51	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F100RDT6	24	Cortex-M3	384	32	0	LQFP64	51	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F100RET6	24	Cortex-M3	512	32	0	LQFP64	51	2-3.6	11	0	1	0	0	1	16	0	0	2	0	0	0	0	3	0	0	2	3+2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

[illegible]

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

[illegible]

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

[illegible]

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

[illegible]

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

[illegible]

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

[illegible]

STM32 F1系列 – Arm® Cortex®-M3基础型MCU

[illegible]

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb Low Power Timer	Nb ADC 12-bit Cell	Nb ADC 16-bit channels	Nb ADC 12-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC					
STM32F301入门型 - 72 MHz																																																								
STM32F301K6U6	72	Cortex-M4	32	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F301K8U6	72	Cortex-M4	64	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F301C6T6	72	Cortex-M4	32	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F301C6Y6	72	Cortex-M4	32	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F301C8T6	72	Cortex-M4	64	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F301C8Y6	72	Cortex-M4	64	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F301R6T6	72	Cortex-M4	32	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F301R8T6	72	Cortex-M4	64	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F302 USB型 - 72 MHz																																																								
STM32F302K6U6	72	Cortex-M4	32	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F302K8U6	72	Cortex-M4	64	16	0	UFQFPN32	24	2-3.6	5	1	1	0	0	1	8	0	0	1	2	1	0	0	2	0	[2]	3	2	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F302C6T6	72	Cortex-M4	32	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F302C6Y6	72	Cortex-M4	32	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F302C8T6	72	Cortex-M4	64	16	0	LQFP48	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F302C8Y6	72	Cortex-M4	64	16	0	WLCSP49	37	2-3.6	5	1	1	0	0	1	11	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F302R6T6	72	Cortex-M4	32	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F302R8T6	72	Cortex-M4	64	16	0	LQFP64	51	2-3.6	5	1	1	0	0	1	15	0	0	1	3	1	0	0	2	0	[2]	3	3	0	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU (附带DSP和FPU)

[illegible]

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU (附带DSP和FPU)

[illegible]

STM32 F3系列 – Arm® Cortex®-M4混合信号MCU (附带DSP和FPU)

[illegible]

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	SHA/MMAC	AES/DES	PKA	OTFDEC	TRNG	RF	SWPMI	DCMI	DPSDM	SPDIFEX	SAI	DSH0ST	3D GPU	JPEG Codec	TFT LCD	Segment LCD	MDIOS	Ethernet	USB OTG_HS	USB OTG_FS	USB Device	FMC	FSMC	SDIO	CAN	LPUART	USART	I2C	I2S	QuadSPI/OctoSPI	SPI	FMAC	Cordic	OPAMP	COMP	Nb DAC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb ADC 12-bit channels	Nb ADC 12-bit Cell	Nb High Resolution Timer	Nb Low Power Timer	Nb motor control Timer (16bits)	Nb Timer (32-bit)	Nb Timer (16-bits)	Supply voltage	IONb	Package Name	E2prom (Bytes)	Ram (Kbytes)	Flash (Kbytes)	Core	Frequency (MHz)	Commercial Product Code		
STM32F378CCT6																			0		0	0	0	0	0	1	0	[3]	2	3	0	3	0	0	0	0	2	3	21	3	9	1	0	0	0	2	12	1.65-1.95	36	LQFP48	0	32	256	Cortex-M4	72	
STM32F378RCT6																			0		0	0	0	0	0	1	0	[3]	2	3	0	3	0	0	0	0	2	3	21	3	16	1	0	0	0	2	12	1.65-1.95	51	LQFP64	0	32	256	Cortex-M4	72	
STM32F378RCY6																			0		0	0	0	0	0	1	0	[3]	2	3	0	3	0	0	0	0	2	3	21	3	16	1	0	0	0	2	12	1.65-1.95	51	WLCSP66	0	32	256	Cortex-M4	72	
STM32F378VCT6																			0		0	0	0	0	0	1	0	[3]	2	3	0	3	0	0	0	0	2	3	21	3	16	1	0	0	0	2	12	1.65-1.95	83	LQFP100	0	32	256	Cortex-M4	72	
STM32F378VCH6																			0		0	0	0	0	0	1	0	[3]	2	3	0	3	0	0	0	0	2	3	21	3	16	1	0	0	0	2	12	1.65-1.95	83	UFBGA100	0	32	256	Cortex-M4	72	
STM32F398VET6																			0		0	0	0	0	1	0	0	[2]	3	3+2	0	4	0	0	0	4	7	2	0	0	0	3	1	10	1.65-1.95	85	LQFP100	0	80	512	Cortex-M4	72				

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IO Nb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 16-bit channels	Nb ADC 12-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC		
STM32F2x5基础型 - 120 MHz																																																				
STM32F205RBT6	120	Cortex-M3	128	64	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F205RCT6	120	Cortex-M3	256	96	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RET6	120	Cortex-M3	512	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205REV6	120	Cortex-M3	512	128	0	WLCSP66	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RFT6	120	Cortex-M3	768	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F205RGT6	120	Cortex-M3	1024	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205RGY6	120	Cortex-M3	1024	128	0	WLCSP66	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205VBT6	120	Cortex-M3	128	64	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205VCT6	120	Cortex-M3	256	96	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205VET6	120	Cortex-M3	512	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205VFT6	120	Cortex-M3	768	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205VGT6	120	Cortex-M3	1024	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205ZCT6	120	Cortex-M3	256	96	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205ZET6	120	Cortex-M3	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205ZFT6	120	Cortex-M3	768	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F205ZGT6	120	Cortex-M3	1024	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F215RET6	120	Cortex-M3	512	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FWC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32F215RGT6	120	Cortex-M3	1024	128	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F215VET6	120	Cortex-M3	512	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F215VGT6	120	Cortex-M3	1024	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F215ZET6	120	Cortex-M3	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F215ZGT6	120	Cortex-M3	1024	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F2x7基础型 - 120 MHz																																																						
STM32F207VCT6	120	Cortex-M3	256	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207VET6	120	Cortex-M3	512	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207VFT6	120	Cortex-M3	768	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207VGT6	120	Cortex-M3	1024	128	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZCT6	120	Cortex-M3	256	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZET6	120	Cortex-M3	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZFT6	120	Cortex-M3	768	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ZGT6	120	Cortex-M3	1024	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207ICT6	120	Cortex-M3	256	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IET6	120	Cortex-M3	512	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IFT6	120	Cortex-M3	768	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F207IGT6	120	Cortex-M3	1024	128	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0

STM32 F2系列 – Arm® Cortex®-M3高性能MCU

[illegible]

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FWC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC							
STM32F400入门型 - 84 MHz																																																											
STM32F400CBT6	100	Cortex-M4	128	32	0	LQFP48	36	1.7-3.6	4	1	1	1	0	1	10	0	0	1	0	0	0	0	3	0	[3]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32F400RBT6	100	Cortex-M4	128	32	0	LQFP64	50	1.8-3.6	4	1	1	1	0	1	16	0	0	1	0	0	0	0	3	0	[3]	3	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F401入门型 - 84 MHz																																																											
STM32F401CBU6	84	Cortex-M4	128	64	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F401CBY6	84	Cortex-M4	128	64	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F401RBT6	84	Cortex-M4	128	64	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F401VBT6	84	Cortex-M4	128	64	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F401VBH6	84	Cortex-M4	128	64	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32F401CCU6	84	Cortex-M4	256	64	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F401CCY6	84	Cortex-M4	256	64	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0				
STM32F401RCT6	84	Cortex-M4	256	64	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F401VCT6	84	Cortex-M4	256	64	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32F401VCH6	84	Cortex-M4	256	64	0	UFBGA100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F401CDU6	84	Cortex-M4	384	96	0	UFQFPN48	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F401CDY6	84	Cortex-M4	384	96	0	WLCSP49	36	1.7-3.6	6	2	1	0	0	1	10	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F401RDT6	84	Cortex-M4	384	96	0	LQFP64	50	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	3	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F401VDT6	84	Cortex-M4	384	96	0	LQFP100	81	1.7-3.6	6	2	1	0	0	1	16	0	0	0	0	0	0	0	4	0	[2]	3	3	0	0	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

[illegible]

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

[illegible]

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

[illegible]

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32F413ZHJ6	100	Cortex-M4	1536	320	0	UFBGA144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	2	0	0	0	0	0	0	0	0	
STM32F423CHU6	100	Cortex-M4	1536	320	0	UFQFPN48	36	1.7-3.6	12	2	2	1	0	1	10	0	0	2	0	0	0	0	5	0	[5]	4	3+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0	
STM32F423RHT6	100	Cortex-M4	1536	320	0	LQFP64	50	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+3	0	3	1	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0		
STM32F423VHT6	100	Cortex-M4	1536	320	0	LQFP100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0		
STM32F423VHJ6	100	Cortex-M4	1536	320	0	UFBGA100	81	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0		
STM32F423ZHT6	100	Cortex-M4	1536	320	0	LQFP144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0	
STM32F423ZHJ6	100	Cortex-M4	1536	320	0	UFBGA144	114	1.7-3.6	12	2	2	1	0	1	16	0	0	2	0	0	0	0	5	1 [Quad]	[5]	4	4+6	0	3	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	2	0	0	0	1	0	0	0	0	
STM32F405 / 415基本型 - 168 MHz																																																								
STM32F405RGT6	168	Cortex-M4	1024	192	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F4050GY6	168	Cortex-M4	1024	192	0	WLCSP90	72	1.8-3.6	12	2	2	0	0	3	13	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F4050EY6	168	Cortex-M4	512	192	0	WLCSP90	72	1.8-3.6	12	2	2	0	0	3	13	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F405VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0
STM32F405ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	
STM32F415RGT6	168	Cortex-M4	1024	192	0	LQFP64	51	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F4150GY6	168	Cortex-M4	1024	192	0	WLCSP90	72	1.8-3.6	12	2	2	0	0	3	13	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F415VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	
STM32F415ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32F407 / 417基本型 - 168 MHz																																																						
STM32F407VET6	168	Cortex-M4	512	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407ZET6	168	Cortex-M4	512	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407IET6	168	Cortex-M4	512	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407IEH6	168	Cortex-M4	512	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407IGT6	168	Cortex-M4	1024	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F407IGH6	168	Cortex-M4	1024	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0
STM32F417VET6	168	Cortex-M4	512	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417VGT6	168	Cortex-M4	1024	192	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417ZET6	168	Cortex-M4	512	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417ZGT6	168	Cortex-M4	1024	192	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IET6	168	Cortex-M4	512	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IEH6	168	Cortex-M4	512	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IGT6	168	Cortex-M4	1024	192	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1
STM32F417IGH6	168	Cortex-M4	1024	192	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	3	0	[2]	3	4+2	0	2	1	1	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC										
STM32F446基本型 - 180 MHz																																																													
STM32F446MCY6	180	Cortex-M4	256	128	0	WLCSP81	63	1.8-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0						
STM32F446MEY6	180	Cortex-M4	512	128	0	WLCSP81	63	1.8-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0						
STM32F446RCT6	180	Cortex-M4	256	128	0	LQFP64	50	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0						
STM32F446RET6	180	Cortex-M4	512	128	0	LQFP64	50	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	0	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0						
STM32F446VCT6	180	Cortex-M4	256	128	0	LQFP100	81	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0	0					
STM32F446VET6	180	Cortex-M4	512	128	0	LQFP100	81	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0	0						
STM32F446ZCT6	180	Cortex-M4	256	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0	0					
STM32F446ZCH6	180	Cortex-M4	256	128	0	UFBGA144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0	0					
STM32F446ZET6	180	Cortex-M4	512	128	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0	0					
STM32F446ZEH6	180	Cortex-M4	512	128	0	UFBGA144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+2	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	0	0	0	0	0	0	0	0	0					
STM32F427 / 437高级型 - 180 MHz																																																													
STM32F427VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F427VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F427ZGT6	180	Cortex-M4	1024	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32F427ZIT6	180	Cortex-M4	2048	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F427AGH6	180	Cortex-M4	1024	256	0	UFBGA169	130	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32F427AIH6	180	Cortex-M4	2048	256	0	UFBGA169	130	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/MAC					
STM32F427IGT6	180	Cortex-M4	1024	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0	0		
STM32F427IGH6	180	Cortex-M4	1024	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	0	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0	0		
STM32F427IIT6	180	Cortex-M4	2048	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0		
STM32F427IIH6	180	Cortex-M4	2048	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0		
STM32F437VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437ZGT6	180	Cortex-M4	1024	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437ZIT6	180	Cortex-M4	2048	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437AIH6	180	Cortex-M4	2048	256	0	UFBGA169	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437IGT6	180	Cortex-M4	1024	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437IGH6	180	Cortex-M4	1024	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437IIT6	180	Cortex-M4	2048	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F437IIH6	180	Cortex-M4	2048	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	0	0	1	0	0	1	0	0	1	0	0	1	1		
STM32F429 / 439高级型 - 180 MHz																																																									
STM32F429VET6	180	Cortex-M4	512	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0
STM32F429ZET6	180	Cortex-M4	512	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	0	0

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

[illegible]

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32F429NGH6	180	Cortex-M4	1024	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	0	0	1	0	0	0	0	1	0	0	1	0	0	0	0	0	0	0		
STM32F429NIH6	180	Cortex-M4	2048	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	0	0	1	0	0	0	0	1	0	0	1	0	0	1	0	0	0	0	
STM32F439VGT6	180	Cortex-M4	1024	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F439VIT6	180	Cortex-M4	2048	256	0	LQFP100	82	1.8-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	4	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F439AIH6	180	Cortex-M4	2048	256	0	UFBGA169	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F439ZGT6	180	Cortex-M4	1024	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F439ZIT6	180	Cortex-M4	2048	256	0	LQFP144	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F439ZIY6	180	Cortex-M4	2048	256	0	WLCSP143	114	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F439IGT6	180	Cortex-M4	1024	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439IGH6	180	Cortex-M4	1024	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439IIT6	180	Cortex-M4	2048	256	0	LQFP176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439IIH6	180	Cortex-M4	2048	256	0	UFBGA176	140	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439BGT6	180	Cortex-M4	1024	256	0	LQFP208	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439BIT6	180	Cortex-M4	2048	256	0	LQFP208	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439NGH6	180	Cortex-M4	1024	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F439NIH6	180	Cortex-M4	2048	256	0	TFBGA216	168	1.8-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	0	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	1	1	1	0	0	1	0	0	0	1	0	0	1	0	0	1	0	0	1	1
STM32F469 / 479高级型 - 180 MHz																																																									
STM32F469VET6	180	Cortex-M4	1024	384	0	LQFP00	71	1.7-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	1	1	1	1	0	0	0	0	1	0	0	1	1	0	0	1	0	0	0	0	0	

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Core & Memory Specifications										Peripheral & Interface Features										Performance & Reliability Metrics																															
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	EEProm (Bbytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	UART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DPSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32F469VGT6	180	Cortex-M4	1024	384	0	LQFP00	71	1.7-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	0	0	1	0	0	1	0	0	0	0	
STM32F469VIT6	180	Cortex-M4	1024	384	0	LQFP00	71	1.7-3.6	12	2	2	0	0	3	14	0	0	2	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469ZET6	180	Cortex-M4	1024	384	0	LQFP144	106	1.7-3.6	12	2	2	0	0	3	20	0	0	2	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469ZGT6	180	Cortex-M4	1024	384	0	LQFP144	106	1.7-3.6	12	2	2	0	0	3	20	0	0	2	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469ZIT6	180	Cortex-M4	1024	384	0	LQFP144	106	1.7-3.6	12	2	2	0	0	3	20	0	0	2	0	0	0	4	1 [Quad]	[2]	3	4+3	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AEH6	180	Cortex-M4	512	384	0	UFPGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AGH6	180	Cortex-M4	1024	384	0	UFPGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AIH6	180	Cortex-M4	2048	384	0	UFPGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AEY6	180	Cortex-M4	512	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AGY6	180	Cortex-M4	1024	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469AIY6	180	Cortex-M4	2048	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0
STM32F469IET6	180	Cortex-M4	512	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	1	1	0	0	1	0	0	1	0	0	1	0	0	0	0	
STM32F469IEH6	180	Cortex-M4	512	384	0	UFPGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	1	1	0	0	1	0	0	1	0	0	1	0	0	0	0	
STM32F469IGT6	180	Cortex-M4	1024	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	0	0
STM32F469IGH6	180	Cortex-M4	1024	384	0	UFPGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	0	0
STM32F469IIT6	180	Cortex-M4	2048	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	1	1	0	0	1	0	0	1	0	0	1	0	0	0	0	
STM32F469IIH6	180	Cortex-M4	2048	384	0	UFPGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	1	1	0	0	1	0	0	1	0	0	1	0	0	0	0	
STM32F469BET6	180	Cortex-M4	512	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	0	0	1	1	0	0	1	0	0	1	0	0	1	0	0	0	0	

STM32 F4系列 – Arm® Cortex®-M4高性能MCU (附带DSP和FPU)

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32F469BGT6	180	Cortex-M4	1024	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	0	0	1	0	0	1	0	0	0	0	0	0		
STM32F469BIT6	180	Cortex-M4	2048	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	
STM32F469NEH6	180	Cortex-M4	512	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	
STM32F469NGH6	180	Cortex-M4	1024	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	
STM32F469NIH6	180	Cortex-M4	2048	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0
STM32F479AGH6	180	Cortex-M4	1024	384	0	UFBGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1	1	1	
STM32F479AIH6	180	Cortex-M4	2048	384	0	UFBGA169	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	0	0	1	1	1	1	
STM32F479AGY6	180	Cortex-M4	1024	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1	
STM32F479AIY6	180	Cortex-M4	2048	384	0	WLCSP168	114	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1	
STM32F479IGT6	180	Cortex-M4	1024	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1
STM32F479IGH6	180	Cortex-M4	1024	384	0	UFBGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1
STM32F479IIT6	180	Cortex-M4	2048	384	0	LQFP176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1
STM32F479IIH6	180	Cortex-M4	2048	384	0	UFBGA176	131	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1
STM32F479BGT6	180	Cortex-M4	1024	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1
STM32F479BIT6	180	Cortex-M4	2048	384	0	LQFP208	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1
STM32F479NGH6	180	Cortex-M4	1024	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1
STM32F479NIH6	180	Cortex-M4	2048	384	0	TFBGA216	161	1.7-3.6	12	2	2	0	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[2]	3	4+4	0	2	1	1	1	0	1	1	1	0	0	1	1	0	0	1	0	0	1	1	0	0	1	0	0	1	1	1	1

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32F7x0超值型 - 216 MHz																																																							
STM32F730R8T6	216	Cortex-M7	64	256+16	0	LQFP64	50	1.7-3.6	11	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	[3]	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0
STM32F730V8T6	216	Cortex-M7	64	256+16	0	LQFP100	82	1.7-3.6	11	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F730Z8T6	216	Cortex-M7	64	256+16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F730I8K6	216	Cortex-M7	64	256+16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	0	1	1	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F750V8T6	216	Cortex-M7	64	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	1	1		
STM32F750Z8T6	216	Cortex-M7	64	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	1	1		
STM32F750N8H6	216	Cortex-M7	64	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	1	1		
STM32F7x2基础型 - 216 MHz																																																							
STM32F722RCT6	216	Cortex-M7	256	256+16	0	LQFP64	50	1.7-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	[3]	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0
STM32F722RET6	216	Cortex-M7	512	256+16	0	LQFP64	50	1.7-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	[3]	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F722VCT6	216	Cortex-M7	256	256+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F722VET6	216	Cortex-M7	512	256+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F722ZCT6	216	Cortex-M7	256	256+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F722ZET6	216	Cortex-M7	512	256+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F722IET6	216	Cortex-M7	512	256+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F722IEK6	216	Cortex-M7	512	256+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	0	0	
STM32F732RET6	216	Cortex-M7	512	256+16	0	LQFP64	50	1.7-3.6	12	2	2	0	0	3	16	0	0	2	0	0	0	0	3	1 [QUAD]	[3]	3	4+2	0	1	1	0	0	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	0	1	0	0	1	0	

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC		
STM32F732VET6	216	Cortex-M7	512	256+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0	
STM32F732ZET6	216	Cortex-M7	512	256+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0		
STM32F732IET6	216	Cortex-M7	512	256+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0			
STM32F732IEK6	216	Cortex-M7	512	256+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	2	0	1	0	1	1	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0			
STM32F7x3基础型 - 216 MHz																																																							
STM32F723VEY6	216	Cortex-M7	512	256+16	0	WLCSP100	79	1.7-3.6	11	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0
STM32F723ZCT6	216	Cortex-M7	256	256+16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0		
STM32F723ZCI6	216	Cortex-M7	256	256+16	0	UFBGA144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0		
STM32F723ZET6	216	Cortex-M7	512	256+16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0		
STM32F723ZEI6	216	Cortex-M7	512	256+16	0	UFBGA144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0			
STM32F723ICT6	216	Cortex-M7	256	256+16	0	LQFP176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0		
STM32F723ICK6	216	Cortex-M7	256	256+16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0			
STM32F723IET6	216	Cortex-M7	512	256+16	0	LQFP176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0		
STM32F723IEK6	216	Cortex-M7	512	256+16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	0	0			
STM32F733VEY6	216	Cortex-M7	512	256+16	0	WLCSP100	79	1.7-3.6	11	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0		
STM32F733ZET6	216	Cortex-M7	512	256+16	0	LQFP144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0		
STM32F733ZEI6	216	Cortex-M7	512	256+16	0	UFBGA144	112	1.7-3.6	11	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0			
STM32F733IET6	216	Cortex-M7	512	256+16	0	LQFP176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	2	0	0	0	0	0	1	0	0	1	0			

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32F733IEK6	216	Cortex-M7	512	256+16	0	UFBGA176	138	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	5	1 [QUAD]	[3]	3	4+4	0	1	1	0	1	0	1	1	0	0	0	0	0	0	0	0	2	0	0	0	0	1	0	0	1	0
STM32F7x5高级型 - 216 MHz																																																					
STM32F745VET6	216	Cortex-M7	512	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745VGT6	216	Cortex-M7	1024	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F745VEH6	216	Cortex-M7	512	320+16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F745VGH6	216	Cortex-M7	1024	320+16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F745ZET6	216	Cortex-M7	512	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745ZGT6	216	Cortex-M7	1024	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745ZEY6	216	Cortex-M7	512	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745ZGY6	216	Cortex-M7	1024	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745IET6	216	Cortex-M7	512	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745IGT6	216	Cortex-M7	1024	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745IEK6	216	Cortex-M7	512	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F745IGK6	216	Cortex-M7	1024	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	2	1	0	1	0	1	1	0	0	0	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F765VGT6	216	Cortex-M7	1024	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765VIT6	216	Cortex-M7	2048	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765ZGT6	216	Cortex-M7	1024	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765ZIT6	216	Cortex-M7	2048	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
																								1 [QUAD]																														
STM32F765IGT6	216	Cortex-M7	1024	512+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765IIT6	216	Cortex-M7	2048	512+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765IGK6	216	Cortex-M7	1024	512+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765IHK6	216	Cortex-M7	2048	512+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765BGT6	216	Cortex-M7	1024	512+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765BIT6	216	Cortex-M7	2048	512+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765NGH6	216	Cortex-M7	1024	512+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F765NIH6	216	Cortex-M7	2048	512+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [QUAD]	[3]	4	4+4	0	3	1	0	1	0	1	1	1	0	0	0	0	0	0	2	1	1	1	0	0	1	0	0	0	0	
STM32F7x6高级型 - 216 MHz																																																						
STM32F746VET6	216	Cortex-M7	512	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0
STM32F746VGT6	216	Cortex-M7	1024	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F746VEH6	216	Cortex-M7	512	320+16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F746VGH6	216	Cortex-M7	1024	320+16	0	TFBGA100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F746ZET6	216	Cortex-M7	512	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F746ZGT6	216	Cortex-M7	1024	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F746ZEY6	216	Cortex-M7	512	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F746ZGY6	216	Cortex-M7	1024	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	
STM32F746IET6	216	Cortex-M7	512	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	2	1	0	1	0	0	1	0	0	0	0	

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSH0ST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC					
STM32F746IGT6	216	Cortex-M7	1024	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	0	0		
STM32F746IEK6	216	Cortex-M7	512	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	0	0		
STM32F746IGK6	216	Cortex-M7	1024	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	0	0		
STM32F746BET6	216	Cortex-M7	512	320+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	0	0		
STM32F746BGT6	216	Cortex-M7	1024	320+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	0	0		
STM32F746NEH6	216	Cortex-M7	512	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	0	0	0	
STM32F746NGH6	216	Cortex-M7	1024	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	0	0	0	0		
STM32F756VGT6	216	Cortex-M7	1024	320+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F756ZGT6	216	Cortex-M7	1024	320+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F756ZGY6	216	Cortex-M7	1024	320+16	0	WLCSP143	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F756IGT6	216	Cortex-M7	1024	320+16	0	LQFP176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F756IGK6	216	Cortex-M7	1024	320+16	0	UFBGA176	140	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F756BGT6	216	Cortex-M7	1024	320+16	0	LQFP208	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F756NGH6	216	Cortex-M7	1024	320+16	0	TFBGA216	168	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	2	1	0	1	0	1	1	1	0	0	1	0	0	0	0	2	1	0	1	0	0	1	0	0	1	0	0	1	1	
STM32F7x7 高级型 - 216 MHz																																																										
STM32F767VGT6	216	Cortex-M7	1024	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0	0	0
STM32F767VIT6	216	Cortex-M7	2048	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0	0	0
STM32F767ZGT6	216	Cortex-M7	1024	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0	0	0

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSH0ST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC		
STM32F767ZIT6	216	Cortex-M7	2048	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767IGT6	216	Cortex-M7	1024	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767IIT6	216	Cortex-M7	2048	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767IGK6	216	Cortex-M7	1024	512+16	0	UFBGA176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767IHK6	216	Cortex-M7	2048	512+16	0	UFBGA176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767BGT6	216	Cortex-M7	1024	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767BIT6	216	Cortex-M7	2048	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767NGH6	216	Cortex-M7	1024	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F767NIH6	216	Cortex-M7	2048	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	0	0		
STM32F777VIT6	216	Cortex-M7	2048	512+16	0	LQFP100	82	1.7-3.6	12	2	2	1	0	3	16	0	0	2	0	0	0	0	4	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	1	1		
STM32F777ZIT6	216	Cortex-M7	2048	512+16	0	LQFP144	114	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	1	1		
STM32F777IIT6	216	Cortex-M7	2048	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	1	1		
STM32F777IHK6	216	Cortex-M7	2048	512+16	0	UFBGA176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	1	1		
STM32F777BIT6	216	Cortex-M7	2048	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	1	1		
STM32F777NIH6	216	Cortex-M7	2048	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	0	2	1	1	1	0	0	1	0	0	1	1		
STM32F7x9高级型 - 216 MHz																																																							
STM32F769AGY6	216	Cortex-M7	1024	512+16	0	WLCSP180	129	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	1	0	0	0	0
STM32F769AIY6	216	Cortex-M7	2048	512+16	0	WLCSP180	129	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	1	0	1	1	0	1	2	1	1	1	0	0	1	0	0	0	0		

STM32 F7系列 – Arm® Cortex®-M7高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	Nb DAC 16-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSH0ST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32F769BGT6	216	Cortex-M7	1024	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	0	0	0		
STM32F769BIT6	216	Cortex-M7	2048	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	0	0	0		
STM32F769IGT6	216	Cortex-M7	1024	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	0	0	0		
STM32F769IIT6	216	Cortex-M7	2048	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	0	0	0		
STM32F769NGH6	216	Cortex-M7	1024	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	0	0	0		
STM32F769NIH6	216	Cortex-M7	2048	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	0	0	0		
STM32F779AIY6	216	Cortex-M7	2048	512+16	0	WLCSP180	129	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	1	0	1	1	
STM32F779BIT6	216	Cortex-M7	2048	512+16	0	LQFP208	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	1	0	1	1	
STM32F779IIT6	216	Cortex-M7	2048	512+16	0	LQFP176	132	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	1	0	1	1	
STM32F779NIH6	216	Cortex-M7	2048	512+16	0	TFBGA216	159	1.7-3.6	12	2	2	1	0	3	24	0	0	2	0	0	0	0	6	1 [Quad]	[3]	4	4+4	0	3	2	0	1	0	1	1	1	0	1	1	0	1	1	0	1	2	1	1	1	0	0	1	0	0	1	1

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

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STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32H742VGH6	480	Cortex-M7	1024	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742ZGT6	480	Cortex-M7	1024	1056	0	LQFP144	114	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742AGI6	480	Cortex-M7	1024	1056	0	UFBGA169	131	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742IGT6	480	Cortex-M7	1024	1056	0	LQFP176	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742IGI6	480	Cortex-M7	1024	1056	0	UFBGA176+25	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742BGT6	480	Cortex-M7	1024	1056	0	LQFP208	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742XGH6	480	Cortex-M7	1024	1056	0	TFBGA240+25	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742VIT6	480	Cortex-M7	2048	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742VIH6	480	Cortex-M7	2048	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742ZIT6	480	Cortex-M7	2048	1056	0	LQFP144	114	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742AII6	480	Cortex-M7	2048	1056	0	UFBGA169	131	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742IIT6	480	Cortex-M7	2048	1056	0	LQFP176	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0
STM32H742III6	480	Cortex-M7	2048	1056	0	UFBGA176+25	140	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32H742BIT6	480	Cortex-M7	2048	1056	0	LQFP208	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H742XIH6	480	Cortex-M7	2048	1056	0	TFBGA240+25	168	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	0	0	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743/753基础型 - 480MHz																																																					
STM32H743VGT6	480	Cortex-M7	1024	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743VGH6	480	Cortex-M7	1024	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743ZGT6	480	Cortex-M7	1024	1056	0	LQFP144	114	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743AGI6	480	Cortex-M7	1024	1056	0	UFBGA169	131	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743IGT6	480	Cortex-M7	1024	1056	0	LQFP176	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743IGI6	480	Cortex-M7	1024	1056	0	UFBGA176+25	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743BGT6	480	Cortex-M7	1024	1056	0	LQFP208	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743XGH6	480	Cortex-M7	1024	1056	0	TFBGA240+25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743VIT6	480	Cortex-M7	2048	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743VIH6	480	Cortex-M7	2048	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPuART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32H743ZIT6	480	Cortex-M7	2048	1056	0	LQFP144	114	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743AIH6	480	Cortex-M7	2048	1056	0	UFBGA169	131	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743IIT6	480	Cortex-M7	2048	1056	0	LQFP176	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743IIH6	480	Cortex-M7	2048	1056	0	UFBGA176+25	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743BIT6	480	Cortex-M7	2048	1056	0	LQFP208	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H743XIH6	480	Cortex-M7	2048	1056	0	TFBGA240+25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0	0
STM32H753VIT6	480	Cortex-M7	2048	1056	0	LQFP100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1	1
STM32H753VIH6	480	Cortex-M7	2048	1056	0	TFBGA100	82	1.71-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1	1
STM32H753ZIT6	480	Cortex-M7	2048	1056	0	LQFP144	114	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1	1
STM32H753AIH6	480	Cortex-M7	2048	1056	0	UFBGA169	131	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1	1
STM32H753IIT6	480	Cortex-M7	2048	1056	0	LQFP176	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1	1
STM32H753IIH6	480	Cortex-M7	2048	1056	0	UFBGA176+25	140	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1	1
STM32H753BIT6	480	Cortex-M7	2048	1056	0	LQFP208	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1	1

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32H753XIH6	480	Cortex-M7	2048	1056	0	TFBGA240+25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1
STM32H745/755双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																								
STM32H745ZGT6	480 & 240	M7 & M4	1024	1056	0	LQFP144	97	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0
STM32H745IGT6	480 & 240	M7 & M4	1024	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745IGK6	480 & 240	M7 & M4	1024	1056	0	UFBGA176+25	128	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745BGT6	480 & 240	M7 & M4	1024	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745XGH6	480 & 240	M7 & M4	1024	1056	0	TFBGA240+25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745ZIT6	480 & 240	M7 & M4	2048	1056	0	LQFP144	97	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745IIT6	480 & 240	M7 & M4	2048	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745IHK6	480 & 240	M7 & M4	2048	1056	0	UFBGA176+25	128	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745BIT6	480 & 240	M7 & M4	2048	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H745XIH6	480 & 240	M7 & M4	2048	1056	0	TFBGA240+25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	0	0			
STM32H755ZIT6	480 & 240	M7 & M4	2048	1056	0	LQFP144	97	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	0	4	1	1	1	1	0	1	0	0	1	1			

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	STM32H747/757双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																						
	SHA/HMAC	AES/DES	PKA	OTFDEC	TRNG	RF	SWPMI	DCMI	DFSDM	SPDIFRX	SAI	DSIHOST	3D GPU	JPEG Codec	TFT LCD	Segment LCD	MDIOS	Ethernet	USB OTG_HS	USB OTG_FS	USB Device	FSMC	SDIO	CAN	LPuART	U(S)ART	I2C	I2S	QuadSPI/OctoSPI	SPI	FMAC	Cordic	OPAMP	COMP	Nb DAC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb ADC 12-bit channels	Nb ADC 12-bit Cell	Nb High Resolution Timer	Nb Low Power Timer	Nb motor control Timer (16bits)	Nb Timer (32-bit)	Nb Timer (16-bits)	Supply voltage	IONb	Package Name	E2prom (Bytes)	Ram (Kbytes)	Flash (Kbytes)	Core	Frequency (MHz)			
STM32H755IIT6	1	1	0	0	1	0	1	1	1	1	4	0	0	1	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	119	LQFP176	0	1056	2048	M7 & M4	480 & 240
STM32H755IIK6	1	1	0	0	1	0	1	1	1	1	4	0	0	1	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	128	UFBGA176 +25	0	1056	2048	M7 & M4	480 & 240
STM32H755BIT6	1	1	0	0	1	0	1	1	1	1	4	0	0	1	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	148	LQFP208	0	1056	2048	M7 & M4	480 & 240		
STM32H755XIH6	1	1	0	0	1	0	1	1	1	1	4	0	0	1	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	168	TFBGA240 +25	0	1056	2048	M7 & M4	480 & 240		
STM32H747AGI6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	112	UFBGA169	0	1056	1024	M7 & M4	480 & 240
STM32H747IGT6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	119	LQFP176	0	1056	1024	M7 & M4	480 & 240		
STM32H747BGT6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	148	LQFP208	0	1056	1024	M7 & M4	480 & 240			
STM32H747XGH6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	168	TFBGA240 +25	0	1056	1024	M7 & M4	480 & 240			
STM32H747ZII6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	99	WLCSP156	0	1056	2048	M7 & M4	480 & 240			
STM32H747AII6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	112	UFBGA169	0	1056	2048	M7 & M4	480 & 240				
STM32H747IIT6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	119	LQFP176	0	1056	2048	M7 & M4	480 & 240				
STM32H747BIT6	0	0	0	0	1	0	1	1	1	1	4	1	1	0	1	0	1	1	1	1	0	1	2	1FD + 1TT	1	4+4	4	[3]	1 [QUAD]	6	0	0	2	2	2	2	2	2	2	2	2	2	12	1.62-3.6	148	LQFP208	0	1056	2048	M7 & M4	480 & 240				

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb ADC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSH0ST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC		
STM32H747XIH6	480 & 240	M7 & M4	2048	1056	0	TFBGA240+25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	1	0	1	4	1	1	1	0	1	0	0	0	0
STM32H757ZIIY6	480 & 240	M7 & M4	2048	1056	0	WLCSP156	99	1.62-3.6	12	2	2	5	1	0	0	3	23	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	1	1	1	1	0	1	0	0	1	1		
STM32H757AII6	480 & 240	M7 & M4	2048	1056	0	UFBGA169	112	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	1	1	1	1	0	1	0	0	1	1		
STM32H757IIT6	480 & 240	M7 & M4	2048	1056	0	LQFP176	119	1.62-3.6	12	2	2	5	1	0	0	3	28	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	1	1	1	1	0	1	0	0	1	1		
STM32H757BIT6	480 & 240	M7 & M4	2048	1056	0	LQFP208	148	1.62-3.6	12	2	2	5	1	0	0	3	32	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	1	1	1	1	0	1	0	0	1	1		
STM32H757XIH6	480 & 240	M7 & M4	2048	1056	0	TFBGA240+25	168	1.62-3.6	12	2	2	5	1	0	0	3	36	2	2	2	0	0	6	1 [QUAD]	[3]	4	4+4	1	1FD + 1TT	2	0	1	0	1	1	1	1	0	1	1	0	1	4	1	1	1	1	0	1	0	0	1	1		
STM32H7A3双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																							
STM32H7A3LIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA225	168	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3LGH6(*)	280	Cortex-M7	1024	1380	0	TFBGA225	168	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3IIK6(*)	280	Cortex-M7	2048	1380	0	UFBGA176+25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3IGK6(*)	280	Cortex-M7	1024	1380	0	UFBGA176+25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3IIT6(*)	280	Cortex-M7	2048	1380	0	LQFP176	119	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3IGT6(*)	280	Cortex-M7	1024	1380	0	LQFP176	119	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		

(*) with SMPS

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32H7A3AIH6(*)	280	Cortex-M7	2048	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0
STM32H7A3AGI6(*)	280	Cortex-M7	1024	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3ZIT6(*)	280	Cortex-M7	2048	1380	0	LQFP144	97	1.62-3.6	12	2	2	3	0	0	0	2	18	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3ZGT6(*)	280	Cortex-M7	1024	1380	0	LQFP144	97	1.62-3.6	12	2	2	3	0	0	0	2	18	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3QIY6(*)	280	Cortex-M7	2048	1380	0	WLCSP132	87	1.62-3.6	12	2	2	3	0	0	0	2	17	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA100	75	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VGH6(*)	280	Cortex-M7	1024	1380	0	TFBGA100	75	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VIT6(*)	280	Cortex-M7	2048	1380	0	LQFP100	68	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	4+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VGT6(*)	280	Cortex-M7	1024	1380	0	LQFP100	68	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	4+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3NIH6	280	Cortex-M7	2048	1380	0	TFBGA216	166	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3NGH6	280	Cortex-M7	1024	1380	0	TFBGA216	166	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3IHK6	280	Cortex-M7	2048	1380	0	UFBGA176+25	138	1.62-3.6	12	2	2	3	0	0	0	2	20	2	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		

(*) with SMPS

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32H7A3IGK6	280	Cortex-M7	1024	1380	0	UFBGA176+25	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0
STM32H7A3IIT6	280	Cortex-M7	2048	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3IGT6	280	Cortex-M7	1024	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3ZIT6	280	Cortex-M7	2048	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3ZGT6	280	Cortex-M7	1024	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VIH6	280	Cortex-M7	2048	1380	0	TFBGA100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VGH6	280	Cortex-M7	1024	1380	0	TFBGA100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VIT6	280	Cortex-M7	2048	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3VGT6	280	Cortex-M7	1024	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7A3RIT6	280	Cortex-M7	2048	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	1	2	1	1	0	1	0	0	0	0		
STM32H7A3RGT6	280	Cortex-M7	1024	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	1	2	1	1	0	1	0	0	0	0		
STM32H723/733双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																							
STM32H723VET6	550	Cortex-M7	512	564	0	LQFP100	80	1.71-3.6	12	4	2	5	0	1	4	2	26	2	2	2	1	1	5	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	STM32H725/735双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																						
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32H723VEH6	550	Cortex-M7	512	564	0	TFBGA100	80	1.62-3.6	12	4	2	5	0	1	17	2	26	2	2	2	1	1	5	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	0	0	0
STM32H723VGT6	550	Cortex-M7	1024	564	0	LQFP100	80	1.71-3.6	12	4	2	5	0	1	4	2	26	2	2	2	1	1	5	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	0	0	0
STM32H723VGH6	550	Cortex-M7	1024	564	0	TFBGA100	80	1.62-3.6	12	4	2	5	0	1	17	2	26	2	2	2	1	1	5	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	0	0	0
STM32H723ZET6	550	Cortex-M7	512	564	0	LQFP144	112	1.62-3.6	12	4	2	5	0	1	12	2	30	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	0	0	0
STM32H723ZEI6	550	Cortex-M7	512	564	0	UFBGA144	114	1.62-3.6	12	4	2	5	0	1	17	2	34	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	0	0	0
STM32H723ZGT6	550	Cortex-M7	1024	564	0	LQFP144	112	1.62-3.6	12	4	2	5	0	1	12	2	30	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	0	0	0
STM32H723ZGI6	550	Cortex-M7	1024	564	0	UFBGA144	114	1.62-3.6	12	4	2	5	0	1	17	2	34	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	0	0	0
STM32H733VGT6	550	Cortex-M7	1024	564	0	LQFP100	80	1.71-3.6	12	4	2	5	0	1	4	2	26	2	2	2	1	1	5	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	0	1	1
STM32H733VGH6	550	Cortex-M7	1024	564	0	TFBGA100	80	1.62-3.6	12	4	2	5	0	1	17	2	26	2	2	2	1	1	5	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	0	1	1
STM32H733ZGT6	550	Cortex-M7	1024	564	0	LQFP144	112	1.62-3.6	12	4	2	5	0	1	12	2	30	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	0	1	1
STM32H733ZGI6	550	Cortex-M7	1024	564	0	UFBGA144	114	1.62-3.6	12	4	2	5	0	1	17	2	34	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	0	1	1

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Features																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																															
	SHA/HMAC	AES/DES	PKA	OTFDEC	TRNG	RF	SWPMI	DCMI	DFSDM	SPDIFRX	SAI	DSHOST	3D GPU	JPEG Codec	TFT LCD	Segment LCD	MDIOS	Ethernet	USB OTG_HS	USB OTG_FS	USB Device	FMC	FSMC	SDIO	CAN	LPuART	U(S)ART	I2C	I2S	QuadSPI/OctoSPI	SPI	FMAC	Cordic	OPAMP	COMP	Nb DAC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb ADC 12-bit channels	Nb ADC 12-bit Cell	Nb High Resolution Timer	Nb Low Power Timer	Nb motor control Timer (16bits)	Nb Timer (32-bit)	Nb Timer (16-bits)	Supply voltage	IONb	Package Name	E2prom (Bytes)	Ram (Kbytes)	Flash (Kbytes)	Core	Frequency (MHz)																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																											
STM32H725RGV6	0	0	0	0	0	1	0	1	1	1	1	2	0	0	0	0	0	1	0	0	1	0	1	0	2	1FD + 1TT	1	3+4	4	[4]	1 [Quad]	4	1	1	1	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2	2

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	STM32H7B0/7B3双核Arm Cortex-M7 + Cortex M4 - 480MHz+240MHz																																																					
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32H725IGT6	550	Cortex-M7	1024	564	0	LQFP176	119	1.62-3.6	12	4	2	5	0	1	12	2	30	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	0	0	0	0	
STM32H735RGV6	550	Cortex-M7	1024	564	0	VQFPN68	46	1.71-3.6	12	4	2	5	0	1	2	2	26	2	2	2	1	1	4	1 [Quad]	[4]	4	3+4	1	1FD + 1TT	2	0	1	0	1	0	1	1	1	0	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1
STM32H735VGT6	550	Cortex-M7	1024	564	0	LQFP100	67	1.71-3.6	12	4	2	5	0	1	4	2	26	2	2	2	1	1	5	2 [Quad]	[4]	5	4+4	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	
STM32H735VGH6	550	Cortex-M7	1024	564	0	TFBGA100	74	1.62-3.6	12	4	2	5	0	1	17	2	26	2	2	2	1	1	5	2 [Octo]	[4]	5	4+6	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	
STM32H735VGY6	550	Cortex-M7	1024	564	0	WLCSP115	67	1.62-3.6	12	4	2	5	0	1	17	2	26	2	2	2	1	1	6	2 [Quad]	[4]	5	4+4	1	2FD + 1TT	2	0	1	0	1	0	1	1	0	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	
STM32H735ZGT6	550	Cortex-M7	1024	564	0	LQFP144	97	1.62-3.6	12	4	2	5	0	1	9	2	28	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	
STM32H735AGI6	550	Cortex-M7	1024	564	0	UFBGA169	121	1.62-3.6	12	4	2	5	0	1	17	2	38	2	2	2	1	1	4	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	
STM32H735IGK6	550	Cortex-M7	1024	564	0	UFBGA176	128	1.62-3.6	12	4	2	5	0	1	17	2	38	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	
STM32H735IGT6	550	Cortex-M7	1024	564	0	LQFP176	119	1.62-3.6	12	4	2	5	0	1	12	2	30	2	2	2	1	1	6	2 [Octo]	[4]	5	5+5	1	2FD + 1TT	2	0	1	0	1	1	1	0	1	1	0	0	0	2	1	1	1	1	0	1	1	0	1	1	
STM32H7B0IBK6(*)	280	Cortex-M7	128	1380	0	UFBGA176+25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0	
STM32H7B0ABI6(*)	280	Cortex-M7	128	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0	
STM32H7B0IBT6	280	Cortex-M7	128	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0	

(*) with SMPS

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSH0ST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32H7B0ZBT6	280	Cortex-M7	128	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0
STM32H7B0VBT6	280	Cortex-M7	128	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	0	0		
STM32H7B0RBT6	280	Cortex-M7	128	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	1	2	1	1	0	1	0	0	0	0		
STM32H7B3LIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA225	168	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3IIK6(*)	280	Cortex-M7	2048	1380	0	UFBGA176 +25	128	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3IIT6(*)	280	Cortex-M7	2048	1380	0	LQFP176	119	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3AII6(*)	280	Cortex-M7	2048	1380	0	UFBGA169	121	1.62-3.6	12	2	2	3	0	0	0	2	24	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3ZIT6(*)	280	Cortex-M7	2048	1380	0	LQFP144	97	1.62-3.6	12	2	2	3	0	0	0	2	18	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3QIY6(*)	280	Cortex-M7	2048	1380	0	WLCSP132	87	1.62-3.6	12	2	2	3	0	0	0	2	17	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3VIH6(*)	280	Cortex-M7	2048	1380	0	TFBGA100	75	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3VIT6(*)	280	Cortex-M7	2048	1380	0	LQFP100	68	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	4+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		
STM32H7B3NIH6	280	Cortex-M7	2048	1380	0	TFBGA216	166	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1		

(*) with SMPS

STM32 H7系列 – Arm® Cortex®-M7 / Arm® Cortex®-M7+M4超高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MII/DS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSI/HOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32H7B3IIK6	280	Cortex-M7	2048	1380	0	UFBGA176+25	138	1.62-3.6	12	2	2	3	0	0	0	2	20	2	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1
STM32H7B3IIT6	280	Cortex-M7	2048	1380	0	LQFP176	138	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	0	1	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1
STM32H7B3ZIT6	280	Cortex-M7	2048	1380	0	LQFP144	112	1.62-3.6	12	2	2	3	0	0	0	2	20	3	2	2	0	0	6	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1
STM32H7B3VIH6	280	Cortex-M7	2048	1380	0	TFBGA100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1
STM32H7B3VIT6	280	Cortex-M7	2048	1380	0	LQFP100	80	1.62-3.6	12	2	2	3	0	0	0	2	16	3	2	2	0	0	5	2 [Octo]	[4]	4	5+5	1	1FD + 1TT	2	1	0	0	[1]	1	0	1	0	1	1	0	0	2	1	2	1	1	0	1	0	0	1	1
STM32H7B3RIT6	280	Cortex-M7	2048	1380	0	LQFP64	49	1.62-3.6	12	2	2	3	0	0	0	2	16	3	1	1	0	0	4	2 [Octo]	[4]	3	4+3	1	1FD + 1TT	2	0	0	0	[1]	1	0	1	0	1	1	0	0	1	1	2	1	1	0	1	0	0	1	1

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

[illegible]

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[illegible]

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

[illegible]

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

[illegible]

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

[illegible]

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

[illegible]

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

[illegible]

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	EEPROM (Bytes)	Package Name	IO pins	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMCC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSH0ST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32L053C8T6	32	Cortex-M0+	64	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L053R6T6	32	Cortex-M0+	32	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L053R6H6	32	Cortex-M0+	32	8	2048	TFBGA64	50	1.65-3.6	4	0	0	1	0	1	15	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L053R8T6	32	Cortex-M0+	64	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L053R8H6	32	Cortex-M0+	64	8	2048	TFBGA64	50	1.65-3.6	4	0	0	1	0	1	15	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L063C8T6	32	Cortex-M0+	64	8	2048	LQFP48	37	1.65-3.6	4	0	0	1	0	1	10	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L063R8T6	32	Cortex-M0+	64	8	2048	LQFP64	51	1.65-3.6	4	0	0	1	0	1	16	0	0	1	2	0	0	0	2	0	1	2	2	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L073CBT6	32	Cortex-M0+	128	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073CZT6	32	Cortex-M0+	192	20	6144	LQFP48	37	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	4x18	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073RBT6	32	Cortex-M0+	128	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073RBH6	32	Cortex-M0+	128	20	6144	TFBGA64	50	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073RZT6	32	Cortex-M0+	192	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073RZH6	32	Cortex-M0+	192	20	6144	TFBGA64	50	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073V8T6	32	Cortex-M0+	64	20	3072	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073V8H6	32	Cortex-M0+	64	20	3072	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073VBT6	32	Cortex-M0+	128	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073VBH6	32	Cortex-M0+	128	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L073VZT6	32	Cortex-M0+	192	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0

STM32 L0系列 – Arm® Cortex®-M0+超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32L073VZI6	32	Cortex-M0+	192	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	15	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L083CBT6	32	Cortex-M0+	128	20	6144	LQFP48	40	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	4x18	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083CZT6	32	Cortex-M0+	192	20	6144	LQFP48	40	1.65-3.6	6	0	0	1	0	1	10	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	4x18	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083RBT6	32	Cortex-M0+	128	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083RZT6	32	Cortex-M0+	192	20	6144	LQFP64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083RZH6	32	Cortex-M0+	192	20	6144	TFBGA64	51	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x28/4x32	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083V8T6	32	Cortex-M0+	64	20	3072	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083V8I6	32	Cortex-M0+	64	20	3072	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083VBT6	32	Cortex-M0+	128	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083VBI6	32	Cortex-M0+	128	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083VZT6	32	Cortex-M0+	192	20	6144	LQFP100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L083VZI6	32	Cortex-M0+	192	20	6144	UFBGA100	84	1.65-3.6	6	0	0	1	0	1	16	0	0	2	2	0	0	0	2	0	1	3	4	1	0	0	0	0	1	0	0	0	0	0	8x48/4x52	0	0	0	0	0	0	0	0	1	0	0	1	0	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	EEProm (Bytes)	Package Name	IO pins	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 16-bit channels	Nb ADC 12-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC							
STM32L100超值型 - 32 MHz																																																										
STM32L100C6U6-A	32	Cortex-M3	32	4	2048	UFQFPN48	37	1.8-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x16	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32L100R8T6-A	32	Cortex-M3	64	8	2048	LQFP64	51	1.8-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32L100RBT6-A	32	Cortex-M3	128	16	2048	LQFP64	51	1.8-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32L100RCT6	32	Cortex-M3	256	16	4096	LQFP64	51	1.8-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	3	0	[2]	2	3	0	0	0	0	0	1	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32L151入门型 - 32 MHz																																																										
STM32L151C6T6-A	32	Cortex-M3	32	16	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32L151C6U6-A	32	Cortex-M3	32	16	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32L151C8T6-A	32	Cortex-M3	64	32	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32L151C8U6-A	32	Cortex-M3	64	32	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32L151CBT6-A	32	Cortex-M3	128	32	4096	LQFP48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32L151CBU6-A	32	Cortex-M3	128	32	4096	UFQFPN48	37	1.65-3.6	8	0	0	0	0	1	14	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32L151R6T6-A	32	Cortex-M3	32	16	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32L151R6H6-A	32	Cortex-M3	32	16	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32L151R8T6-A	32	Cortex-M3	64	32	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32L151R8H6-A	32	Cortex-M3	64	32	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
STM32L151RBT6-A	32	Cortex-M3	128	32	4096	LQFP64	51	1.65-3.6	8	0	0	0	0	1	20	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
STM32L151RBH6-A	32	Cortex-M3	128	32	4096	TFBGA64	50	1.65-3.6	8	0	0	0	0	1	19	0	0	2	2	0	0	0	2	0	0	2	3	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Core & Memory Specifications										Analog & Mixed-Signal Features										Digital & Interface Capabilities										System & Security Features																													
	Frequency (MHz)	Core	Flash (Kbytes)	RAM (Kbytes)	EEProm (Bytes)	Package Name	IO Nb	Supply voltage	Nb Timer (16-bits)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	UART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DPSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC										
STM32L151V8T6-A	32	Cortex-M3	64	32	4096	LQFP100	83	1.65-3.6	8	0	0	0	0	0	0	1	24	0	0	2	2	0	0	2	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
STM32L151V8H6-A	32	Cortex-M3	64	32	4096	UFBGA100	83	1.65-3.6	8	0	0	0	0	0	1	24	0	0	2	2	0	0	0	2	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
STM32L151VBT6-A	32	Cortex-M3	128	32	4096	LQFP100	83	1.65-3.6	8	0	0	0	0	0	1	24	0	0	2	2	0	0	0	2	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0								
STM32L151VBH6-A	32	Cortex-M3	128	32	4096	UFBGA100	83	1.65-3.6	8	0	0	0	0	0	1	24	0	0	2	2	0	0	0	2	3	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0							
STM32L151CCT6	32	Cortex-M3	256	32	8192	LQFP48	37	1.65-3.6	8	1	0	0	0	0	1	14	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32L151CCU6	32	Cortex-M3	256	32	8192	UFQFPN48	37	1.65-3.6	8	1	0	0	0	0	1	14	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32L151UCY6	32	Cortex-M3	256	32	8192	WLCSP63	51	1.65-3.6	8	1	0	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32L151RCT6	32	Cortex-M3	256	32	8192	LQFP64	51	1.65-3.6	8	1	0	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32L151VCT6	32	Cortex-M3	256	32	8192	LQFP100	83	1.65-3.6	8	1	0	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32L151VCH6	32	Cortex-M3	256	32	8192	UFBGA100	83	1.65-3.6	8	1	0	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32L151QCH6	32	Cortex-M3	256	32	8192	UFBGA132	109	1.65-3.6	8	1	0	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0						
STM32L151ZCT6	32	Cortex-M3	256	32	8192	LQFP144	115	1.65-3.6	8	1	0	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0					
STM32L151RDT6	32	Cortex-M3	384	48	12288	LQFP64	51	1.65-3.6	8	1	0	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32L151RDY6	32	Cortex-M3	384	48	12288	WLCSP64	51	1.65-3.6	8	1	0	0	0	0	1	21	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0			
STM32L151VDT6	32	Cortex-M3	384	48	12288	LQFP100	83	1.65-3.6	8	1	0	0	0	0	1	25	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32L151QDH6	32	Cortex-M3	384	48	12288	UFBGA132	109	1.65-3.6	8	1	0	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
STM32L151ZDT6	32	Cortex-M3	384	48	12288	LQFP144	115	1.65-3.6	8	1	0	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

[illegible]

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

[illegible]

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

[illegible]

STM32 L1系列 – Arm® Cortex®-M3超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FWC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L162QDH6	32	Cortex-M3	384	48	12288	UFBGA132	109	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162ZDT6	32	Cortex-M3	384	48	12288	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	3	0	0	3	0	2	2	5	0	0	1	1	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VDY6-X	32	Cortex-M3	384	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162RET6	32	Cortex-M3	512	80	16384	LQFP64	51	1.65-3.6	8	1	0	0	0	1	21	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	4x32/8x28	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VET6	32	Cortex-M3	512	80	16384	LQFP100	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162VEY6	32	Cortex-M3	512	80	16384	WLCSP104	83	1.65-3.6	8	1	0	0	0	1	25	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	1	0
STM32L162ZET6	32	Cortex-M3	512	80	16384	LQFP144	115	1.65-3.6	8	1	0	0	0	1	40	0	0	2	2	2	0	0	3	0	2	2	5	0	0	0	0	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	0	0	0	0	0	0	0	1	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb Low Power Timer	Nb ADC 12-bit Cell	Nb ADC 16-bit channels	Nb ADC 12-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32L4x1 入门型 - 80 MHz																																																							
STM32L431KBU6	80	Cortex-M4	128	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431KCU6	80	Cortex-M4	256	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431CBT6	80	Cortex-M4	128	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431CCT6	80	Cortex-M4	256	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431CBU6	80	Cortex-M4	128	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431CCU6	80	Cortex-M4	256	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431CBY6	80	Cortex-M4	128	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431CCY6	80	Cortex-M4	256	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431RBT6	80	Cortex-M4	128	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431RCT6	80	Cortex-M4	256	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431RBI6	80	Cortex-M4	128	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431RCI6	80	Cortex-M4	256	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431RBY6	80	Cortex-M4	128	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431RCY6	80	Cortex-M4	256	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431VCT6	80	Cortex-M4	256	64	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L431VCI6	80	Cortex-M4	256	64	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L451CCU6	80	Cortex-M4	256	160	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	1	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Core & Memory Specifications										Analog & Mixed-Signal Features										Digital & Interface Capabilities										System & Peripheral Details																				
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	EEProm (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadsPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPII/RX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32L451CEU6	80	Cortex-M4	512	160	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0	0	0	0
STM32L451RCT6	80	Cortex-M4	256	160	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L451RCI6	80	Cortex-M4	256	160	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L451REY6	80	Cortex-M4	512	160	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L451REI6	80	Cortex-M4	512	160	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L451RET6	80	Cortex-M4	512	160	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L451VCT6	80	Cortex-M4	256	160	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0
STM32L451VCI6	80	Cortex-M4	256	160	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L451VET6	80	Cortex-M4	512	160	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L451VEI6	80	Cortex-M4	512	160	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	1	2	1	0	0	3	1 [Quad]	0	4	3+1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	0	0	0
STM32L471RET6	80	Cortex-M4	512	128	0	LQFP64	51	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0
STM32L471RGT6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0
STM32L471VET6	80	Cortex-M4	512	128	0	LQFP100	82	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0
STM32L471VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	9	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0
STM32L471QEI6	80	Cortex-M4	512	128	0	UFBGA132	109	1.71-3.6	9	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0
STM32L471QGI6	80	Cortex-M4	1024	128	0	UFBGA132	109	1.71-3.6	9	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0	
STM32L471ZET6	80	Cortex-M4	512	128	0	LQFP144	114	1.71-3.6	9	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	1	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FWC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC		
STM32L471ZGT6	80	Cortex-M4	1024	128	0	LQFP144	114	1.71-3.6	9	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0	0
STM32L4x2 USB FS产品线 - 80 MHz																																																						
STM32L412KBT6	80	Cortex-M4	128	40	0	LQFP32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L412KBU6	80	Cortex-M4	128	40	0	UFQFPN32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L412TBY6	80	Cortex-M4	128	40	0	WLCSP36	30	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L412CBT6	80	Cortex-M4	128	40	0	LQFP48	38	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
STM32L412CBU6	80	Cortex-M4	128	40	0	UFQFPN48	38	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	
STM32L412RBT6	80	Cortex-M4	128	40	0	LQFP64	52	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	
STM32L412RBI6	80	Cortex-M4	128	40	0	UFBGA64	52	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	
STM32L422KBT6	80	Cortex-M4	128	40	0	LQFP32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L422KBU6	80	Cortex-M4	128	40	0	UFQFPN32	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L422TBY6	80	Cortex-M4	128	40	0	WLCSP36	30	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	1	1 [Quad]	0	2	2	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L422CBT6	80	Cortex-M4	128	40	0	LQFP48	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	
STM32L422CBU6	80	Cortex-M4	128	40	0	UFQFPN48	26	1.71-3.6	4	1	1	2	0	2	10	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
STM32L422RBT6	80	Cortex-M4	128	40	0	LQFP64	26	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	
STM32L422RBI6	80	Cortex-M4	128	40	0	UFBGA64	26	1.71-3.6	4	1	1	2	0	2	16	0	0	0	1	1	0	0	2	1 [Quad]	0	3	3	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	
STM32L432KBU6	80	Cortex-M4	128	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0
STM32L432KCU6	80	Cortex-M4	256	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	Eeprom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTPDEC	PKA	AES/DES	SHA/HMAC																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																																	
STM32L442KCU6	80	Cortex-M4	256	64	0	UFQFPN32	26	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	2	1 [Quad]	0	2	2	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32L4x3 USB FS & LCD产品线 - 80 MHz																																																							
STM32L433CBT6	80	Cortex-M4	128	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0
STM32L433CBU6	80	Cortex-M4	128	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433CBY6	80	Cortex-M4	128	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433CCT6	80	Cortex-M4	256	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433CCU6	80	Cortex-M4	256	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433CCY6	80	Cortex-M4	256	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433RBT6	80	Cortex-M4	128	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433RBI6	80	Cortex-M4	128	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433RBY6	80	Cortex-M4	128	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433RCT6	80	Cortex-M4	256	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433RCI6	80	Cortex-M4	256	64	0	UFBGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433RCY6	80	Cortex-M4	256	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x32/8x28	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433VCT6	80	Cortex-M4	256	64	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L433VCI6	80	Cortex-M4	256	64	0	UFBGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	4x44/8x40	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	
STM32L443CCT6	80	Cortex-M4	256	64	0	LQFP48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	
STM32L443CCU6	80	Cortex-M4	256	64	0	UFQFPN48	38	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	
STM32L443CCY6	80	Cortex-M4	256	64	0	WLCSP49	39	1.71-3.6	5	1	1	2	0	1	10	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	0	0	0	1	0	0	0	0	0	4x19	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	STM32L4x5 USB OTG产品线 - 80 MHz																																																						
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32L443RCT6	80	Cortex-M4	256	64	0	LQFP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0
STM32L443RCI6	80	Cortex-M4	256	64	0	UFPGA64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	
STM32L443RCY6	80	Cortex-M4	256	64	0	WLCSP64	52	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	
STM32L443VCT6	80	Cortex-M4	256	64	0	LQFP100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	
STM32L443VCI6	80	Cortex-M4	256	64	0	UFPGA100	83	1.71-3.6	5	1	1	2	0	1	16	0	0	2	2	1	0	0	3	1 [Quad]	0	3	3	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0	
STM32L4x5 USB OTG产品线 - 80 MHz																																																							
STM32L475RCT6	80	Cortex-M4	256	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L475RET6	80	Cortex-M4	512	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L475RG6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L475VCT6	80	Cortex-M4	256	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L475VET6	80	Cortex-M4	512	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L475VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L4x6 USB OTG & LCD产品线 - 80 MHz																																																							
STM32L476RCT6	80	Cortex-M4	256	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476RET6	80	Cortex-M4	512	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476RG6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476JEY6	80	Cortex-M4	512	128	0	WLCSP72	57	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476JGY6	80	Cortex-M4	1024	128	0	WLCSP72	57	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	1	0	0	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32L476MEY6	80	Cortex-M4	512	128	0	WLCSP81	65	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	1	0	0	0	8x30/4x32	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476MGY6	80	Cortex-M4	1024	128	0	WLCSP81	65	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	1	0	0	0	8x30/4x32	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476VCT6	80	Cortex-M4	256	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476VET6	80	Cortex-M4	512	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476QEI6	80	Cortex-M4	512	128	0	UFBGA132	109	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476QGI6	80	Cortex-M4	1024	128	0	UFBGA132	109	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476ZET6	80	Cortex-M4	512	128	0	LQFP144	114	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L476ZGT6	80	Cortex-M4	1024	128	0	LQFP144	114	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	0	0	0
STM32L486RGT6	80	Cortex-M4	1024	128	0	LQFP64	51	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	1	0	0	0	8x28/4x32	0	0	0	0	2	0	1	0	1	0	0	1	0	0
STM32L486JGY6	80	Cortex-M4	1024	128	0	WLCSP72	57	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	0	0	0	0	1	0	0	0	8x28/4x32	0	0	0	0	2	0	1	0	1	0	0	1	0	0
STM32L486VGT6	80	Cortex-M4	1024	128	0	LQFP100	82	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	1	0	0
STM32L486QGI6	80	Cortex-M4	1024	128	0	UFBGA132	109	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	1	0	0
STM32L486ZGT6	80	Cortex-M4	1024	128	0	LQFP144	114	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	3	3+2	1	1	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	0	1	0	0	1	0	0
STM32L496RET6	80	Cortex-M4	512	320	0	LQFP64	52	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	0	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0
STM32L496RGT6	80	Cortex-M4	1024	320	0	LQFP64	52	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	0	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0
STM32L496VET6	80	Cortex-M4	512	320	0	LQFP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	0	1	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0

STM32 L4系列 – Arm® Cortex®-M4超低功耗MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FWC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC		
STM32L496VGT6	80	Cortex-M4	1024	320	0	LQFP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496VGY6	80	Cortex-M4	1024	320	0	WLCSP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496QEI6	80	Cortex-M4	512	320	0	UFBGA132	110	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496QGI6	80	Cortex-M4	1024	320	0	UFBGA132	110	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496ZET6	80	Cortex-M4	512	320	0	LQFP144	115	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496ZGT6	80	Cortex-M4	1024	320	0	LQFP144	115	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496AEI6	80	Cortex-M4	512	320	0	UFBGA169	136	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L496AGI6	80	Cortex-M4	1024	320	0	UFBGA169	136	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	0	0
STM32L4A6RGT6	80	Cortex-M4	1024	320	0	LQFP64	52	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	0	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6VGT6	80	Cortex-M4	1024	320	0	LQFP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6VGY6	80	Cortex-M4	1024	320	0	WLCSP100	83	1.71-3.6	11	2	2	2	0	3	16	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6QGI6	80	Cortex-M4	1024	320	0	UFBGA132	110	1.71-3.6	11	2	2	2	0	3	19	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6ZGT6	80	Cortex-M4	1024	320	0	LQFP144	115	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1
STM32L4A6AGI6	80	Cortex-M4	1024	320	0	UFBGA169	136	1.71-3.6	11	2	2	2	0	3	24	0	0	2	2	2	0	0	3	1 [Quad]	0	4	3+2	1	2	1	1	0	0	1	0	0	0	0	8x40/4x44	0	0	0	0	2	0	1	1	1	0	1	0	0	1	1

STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32L4P5/4Q5系列：带USB OTG - 120 MHz																																																							
STM32L4P5CGT6	120	Cortex-M4	1024	320	0	LQFP48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	0	1	0	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1		
STM32L4P5CGU6	120	Cortex-M4	1024	320	0	UFQFPN48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5CET6	120	Cortex-M4	512	320	0	LQFP48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5CEU6	120	Cortex-M4	512	320	0	UFQFPN48	38	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5RGT6	120	Cortex-M4	1024	320	0	LQFP64	52	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5RET6	120	Cortex-M4	512	320	0	LQFP64	52	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	0	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5VGT6	120	Cortex-M4	1024	320	0	LQFP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5VGY6	120	Cortex-M4	1024	320	0	WLCSP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5VET6	120	Cortex-M4	512	320	0	LQFP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5VEY6	120	Cortex-M4	512	320	0	WLCSP100	83	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5QGI6	120	Cortex-M4	1024	320	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5QEI6	120	Cortex-M4	512	320	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5ZGT6	120	Cortex-M4	1024	320	0	LQFP144	115	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5ZET6	120	Cortex-M4	512	320	0	LQFP144	115	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5AGI6	120	Cortex-M4	1024	320	0	UFBGA169	136	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1			
STM32L4P5AEI6	120	Cortex-M4	512	320	0	UFBGA169	136	1.71-3.6	9	2	2	2	0	2	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	2	1	0	0	0	1	0	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	1		

STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
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STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	STM32L4 Series - 120 MHz Performance & Features																																																										
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	EEProm (Bytes)	Package Name	IO Nb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SMPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/MAC						
STM32L4R5AGI6	120	Cortex-M4	1024	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	0	0	0	0	0	0		
STM32L4R5AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	0	0	0	0	0	
STM32L4S5VIT6	120	Cortex-M4	2048	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1				
STM32L4S5QII6	120	Cortex-M4	2048	640	0	UFBGA132	110	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1					
STM32L4S5ZIT6	120	Cortex-M4	2048	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1					
STM32L4S5ZIY6	120	Cortex-M4	2048	640	0	WLCSP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1					
STM32L4S5AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1					
STM32L4R7/4S7系列：带USB OTG与TFT接口 - 120 MHz																																																											
STM32L4R7VIT6	120	Cortex-M4	2048	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	0	0	0
STM32L4R7ZIT6	120	Cortex-M4	2048	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	0	0		
STM32L4R7AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	0	0		
STM32L4S7VIT6	120	Cortex-M4	2048	640	0	LQFP100	83	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	1	0	0	1	0	0	1	1				
STM32L4S7ZIT6	120	Cortex-M4	2048	640	0	LQFP144	115	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1		
STM32L4S7AII6	120	Cortex-M4	2048	640	0	UFBGA169	140	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	0	2	0	1	1	0	0	1	0	0	1	1	
STM32L4R9/4S9系列：带USB OTG与TFT接口 - 120 MHz																																																											
STM32L4R9VGT6	120	Cortex-M4	1024	640	0	LQFP100	77	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	1	2	0	1	1	0	0	1	0	0	0	0	0
STM32L4R9VIT6	120	Cortex-M4	2048	640	0	LQFP100	77	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	2	0	1	1	0	0	1	0	0	0	0	0	
STM32L4R9ZGT6	120	Cortex-M4	1024	640	0	LQFP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0	1	2	0	1	1	0	0	1	0	0	0	0	0	

STM32 L4+系列 – Arm® Cortex®-M4超低功耗高性能MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMG	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32L4R9ZIT6	120	Cortex-M4	2048	640	0	LQFP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	0	0	0
STM32L4R9ZGJ6	120	Cortex-M4	1024	640	0	UFBGA144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	0	0	0
STM32L4R9ZIJ6	120	Cortex-M4	2048	640	0	UFBGA144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	0	0	0
STM32L4R9ZGY6	120	Cortex-M4	1024	640	0	WLCSP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	0	0	0
STM32L4R9ZII6	120	Cortex-M4	2048	640	0	WLCSP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	0	0	0
STM32L4R9AGI6	120	Cortex-M4	1024	640	0	UFBGA169	131	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	0	0	0
STM32L4R9AII6	120	Cortex-M4	2048	640	0	UFBGA169	131	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	0	0	0
STM32L4S9VIT6	120	Cortex-M4	2048	640	0	LQFP100	77	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	1	1	1
STM32L4S9ZIT6	120	Cortex-M4	2048	640	0	LQFP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	1	1	1
STM32L4S9ZIJ6	120	Cortex-M4	2048	640	0	UFBGA144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	1	1	1
STM32L4S9ZII6	120	Cortex-M4	2048	640	0	WLCSP144	112	1.71-3.6	9	2	2	2	0	1	16	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	1	1	1
STM32L4S9AII6	120	Cortex-M4	2048	640	0	UFBGA169	131	1.71-3.6	9	2	2	2	0	1	14	0	0	2	2	2	0	0	3	2 [Octo]	0	4	3+2	1	1	1	1	0	0	1	0	0	0	0	0	1	0	0	1	2	0	1	1	0	0	1	1	1

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能安全MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32L5x2系列 - 110 MHz																																																							
STM32L552CCT6	110	Cortex-M33	256	256	0	LQFP48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1
STM32L552CCU6	110	Cortex-M33	256	256	0	UFQFN48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552CET6	110	Cortex-M33	512	256	0	LQFP48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552CEU6	110	Cortex-M33	512	256	0	UFQFN48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552CET6P	110	Cortex-M33	512	256	0	LQFP48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552CEU6P	110	Cortex-M33	512	256	0	UFQFN48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552RCT6	110	Cortex-M33	256	256	0	LQFP64	52	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552RET6	110	Cortex-M33	512	256	0	LQFP64	52	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552RET6P	110	Cortex-M33	512	256	0	LQFP64	50	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552RET6Q	110	Cortex-M33	512	256	0	LQFP64	47	1.71-3.6	9	2	2	3	0	2	15	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552MEY6P	110	Cortex-M33	512	256	0	WLCSP81	54	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552MEY6Q	110	Cortex-M33	512	256	0	WLCSP81	51	1.71-3.6	9	2	2	3	0	2	15	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552VET6	110	Cortex-M33	512	256	0	LQFP100	83	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552VCT6Q	110	Cortex-M33	512	256	0	LQFP100	79	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552VET6Q	110	Cortex-M33	512	256	0	LQFP100	79	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552QEI6P	110	Cortex-M33	512	256	0	UFBGA132	108	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	
STM32L552QCI6Q	110	Cortex-M33	256	256	0	UFBGA132	105	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	0	1	1	1	0	1	

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能安全MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32L552QE16Q	110	Cortex-M33	512	256	0	UFBGA132	105	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	0	1
STM32L552ZET6	110	Cortex-M33	512	256	0	LQFP144	115	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	0	1	
STM32L552ZET6Q	110	Cortex-M33	512	256	0	LQFP144	111	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	0	1	
STM32L552ZCT6Q	110	Cortex-M33	256	256	0	LQFP144	111	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	0	1	
STM32L562CET6	110	Cortex-M33	512	256	0	LQFP48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1	
STM32L562CEU6	110	Cortex-M33	512	256	0	UFQFN48	38	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1	
STM32L562CET6P	110	Cortex-M33	512	256	0	LQFP48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1	
STM32L562CEU6P	110	Cortex-M33	512	256	0	UFQFN48	36	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	2+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1	
STM32L562RET6	110	Cortex-M33	512	256	0	LQFP64	52	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1	
STM32L562RET6P	110	Cortex-M33	512	256	0	LQFP64	50	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	0	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		
STM32L562RET6Q	110	Cortex-M33	512	256	0	LQFP64	47	1.71-3.6	9	2	2	3	0	2	9	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		
STM32L562MEY6P	110	Cortex-M33	512	256	0	WLCSP81	54	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		
STM32L562MEY6Q	110	Cortex-M33	512	256	0	WLCSP81	51	1.71-3.6	9	2	2	3	0	2	15	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	0	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		
STM32L562VET6	110	Cortex-M33	512	256	0	LQFP100	83	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		
STM32L562VET6Q	110	Cortex-M33	512	256	0	LQFP100	79	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		
STM32L562QE16	110	Cortex-M33	512	256	0	UFBGA132	110	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		
STM32L562QE16P	110	Cortex-M33	512	256	0	UFBGA132	108	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	1		

STM32 L5系列 – Arm® Cortex®-M33超低功耗高性能安全MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32L562QE16Q	110	Cortex-M33	512	256	0	UFBGA132	105	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1
STM32L562ZET6	110	Cortex-M33	512	256	0	LQFP144	115	1.71-3.6	9	2	2	3	0	2	16	0	0	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	
STM32L562ZET6Q	110	Cortex-M33	512	256	0	LQFP144	111	1.71-3.6	9	2	2	3	0	2	14	0	0	2	2	0	0	3	1 [Octo]	0	4	3+2	1	1 [FD]	1	1	0	1	0	0	0	0	0	0	0	0	2	0	1	0	0	1	1	1	1	

STM32 U5系列 – Arm® Cortex®-M33超低功耗高性能安全MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC				
STM32L5x5系列 - 110 MHz																																																							
STM32U575CIT6	160	Cortex-M33	2048	786	0	LQFP48	36	1.71-3.6	7	4	2	4	0	2	11	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+1	1 [FD]	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	1		
STM32U575CIU6	160	Cortex-M33	2048	786	0	UFQFPN48	36	1.71-3.6	7	4	2	4	0	2	11	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+1	1 [FD]	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	1			
STM32U575RIT6	160	Cortex-M33	2048	786	0	LQFP64	50	1.71-3.6	7	4	2	4	0	2	17	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	1			
STM32U575OIY6	160	Cortex-M33	2048	786	0	WLCSP90	69	1.71-3.6	7	4	2	4	0	2	16	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	0	1		
STM32U575VIT6	160	Cortex-M33	2048	786	0	LQFP100	81	1.71-3.6	7	4	2	4	0	2	20	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	0	0	1		
STM32U575QII6	160	Cortex-M33	2048	786	0	UFBGA132	109	1.71-3.6	7	4	2	4	0	2	24	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	0	0	1			
STM32U575ZIT6	160	Cortex-M33	2048	786	0	LQFP144	113	1.71-3.6	7	4	2	4	0	2	24	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	0	0	1			
STM32U575AII6	160	Cortex-M33	2048	786	0	UFBGA169	137	1.71-3.6	7	4	2	4	0	2	24	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	0	0	1			
STM32U585CIT6	160	Cortex-M33	2048	786	0	LQFP48	36	1.71-3.6	7	4	2	4	0	2	11	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+1	1 [FD]	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1	1	1	1
STM32U585CIU6	160	Cortex-M33	2048	786	0	UFQFPN48	36	1.71-3.6	7	4	2	4	0	2	11	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+1	1 [FD]	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1	1	1	1
STM32U585RIT6	160	Cortex-M33	2048	786	0	LQFP64	50	1.71-3.6	7	4	2	4	0	2	17	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1	1	1	1
STM32U585OIY6	160	Cortex-M33	2048	786	0	WLCSP90	69	1.71-3.6	7	4	2	4	0	2	16	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1	1	1	1	1
STM32U585VIT6	160	Cortex-M33	2048	786	0	LQFP100	81	1.71-3.6	7	4	2	4	0	2	20	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	1	1	1	1	1
STM32U585QII6	160	Cortex-M33	2048	786	0	UFBGA132	109	1.71-3.6	7	4	2	4	0	2	24	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	1	1	1	1	1
STM32U585ZIT6	160	Cortex-M33	2048	786	0	LQFP144	113	1.71-3.6	7	4	2	4	0	2	24	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	1	1	1	1	1
STM32U585AII6	160	Cortex-M33	2048	786	0	UFBGA169	137	1.71-3.6	7	4	2	4	0	2	24	0	0	2	2	2	1	1	3	2 [Octo]	0	4	3+2	1 [FD]	2	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	2	0	0	1	0	0	1	1	1	1	1

STM32 WB系列 – Arm® Cortex®-M4和Cortex®-M0+双核无线MCU

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STM32 WB系列 – Arm® Cortex®-M4和Cortex®-M0+双核无线MCU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IO Nb	Supply voltage	Nb Timer (16-bits)	Nb timer control Timer (16bits)	Nb High Resolution Timer	Nb Low Power Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMCC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC		
STM32WB55REV6	64	M4 & M0+	1024	512	0	VQFPN68	49	1.71-3.6	3	1	1	2	0	1	19	0	0	0	2	0	0	0	2	1 [Quad]	0	2	1	1	0	0	0	0	1	0	0	0	0	0	4x28	0	0	0	0	1	0	0	0	0	1	1	0	1	1	0
STM32WB55VCQ6	64	M4 & M0+	256	256	0	UFBGA129	72	1.71-3.6	3	1	1	2	0	1	19	0	0	0	2	0	0	0	2	1 [Quad]	0	2	1	1	0	0	0	0	1	0	0	0	0	0	8x40/4x44	0	0	0	0	1	0	0	0	0	1	1	0	1	1	0
STM32WB55VEQ6	64	M4 & M0+	512	512	0	UFBGA129	72	1.71-3.6	3	1	1	2	0	1	19	0	0	0	2	0	0	0	2	1 [Quad]	0	2	1	1	0	0	0	0	1	0	0	0	0	0	8x40/4x44	0	0	0	0	1	0	0	0	0	1	1	0	1	1	0
STM32WB55VGQ6	64	M4 & M0+	1024	512	0	UFBGA129	72	1.71-3.6	3	1	1	2	0	1	19	0	0	0	2	0	0	0	2	1 [Quad]	0	2	1	1	0	0	0	0	1	0	0	0	0	0	8x40/4x44	0	0	0	0	1	0	0	0	0	1	1	0	1	1	0
STM32WB55VY6	64	M4 & M0+	640	512	0	WLCSP100	72	1.71-3.6	3	1	1	2	0	1	19	0	0	0	2	0	0	0	2	1 [Quad]	0	2	1	1	0	0	0	0	1	0	0	0	0	0	8x40/4x44	0	0	0	0	1	0	0	0	0	1	1	0	1	1	0

STM32 WL系列 - Arm® Cortex®-M4 / Arm® Cortex®-M4+M0+长距离无线SoC

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb Low Power Timer	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32WL5系列 - Cortex-M4 48 MHz支持LoRa®、(G)FSK、(G)MSK和BPSK																																																						
STM32WL54CCU6	48	M4 & M0+	256	64	0	UFQFPN48	29	1.8-3.6	3	1	1	3	0	1	13	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0		
STM32WL54JC16	48	M4 & M0+	256	64	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0			
STM32WL55CCU6	48	M4 & M0+	256	64	0	UFQFPN48	29	1.8-3.6	3	1	1	3	0	1	13	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0			
STM32WL55JC16	48	M4 & M0+	256	64	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0			
STM32WLE系列 - Cortex-M4 48 MHz支持LoRa®、(G)FSK、(G)MSK和BPSK																																																						
STM32WLE4CCU6	48	Cortex M4	256	64	0	UFQFPN48	29	1.8-3.6	3	1	1	3	0	1	13	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0	
STM32WLE4JC16	48	Cortex M4	256	64	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0		
STM32WLE5C8U6	48	Cortex M4	64	20	0	UFQFPN48	29	1.8-3.6	3	1	1	3	0	1	13	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0	
STM32WLE5CBU6	48	Cortex M4	128	48	0	UFQFPN48	29	1.8-3.6	3	1	1	3	0	1	13	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0	
STM32WLE5CCU6	48	Cortex M4	256	64	0	UFQFPN48	29	1.8-3.6	3	1	1	3	0	1	13	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0	
STM32WLE5J816	48	Cortex M4	64	20	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0	
STM32WLE5JB16	48	Cortex M4	128	48	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0	
STM32WLE5JC16	48	Cortex M4	256	64	0	UFBGA73	43	1.8-3.6	3	1	1	3	0	1	16	0	0	1	2	0	0	2	0	[1]	3	2	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0	

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC			
STM32MP151系列- Arm Cortex-A7 + Cortex-M4																																																							
STM32MP151AAD1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		
STM32MP151AAB1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		
STM32MP151AAC1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		
STM32MP151AAA1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2[HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		
STM32MP151CAD1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1		
STM32MP151CAB1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1		
STM32MP151CAC1	650 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1		
STM32MP151CAA1	650 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1		
STM32MP151DAD1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		
STM32MP151DAB1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		
STM32MP151DAC1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		
STM32MP151DAA1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1		

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	STM32MP153系列- 双核Arm Cortex-A7 + Cortex-M4																																																				
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPuART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32MP151FAD1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP151FAB1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP151FAC1	800 + 209	Cortex A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP151FAA1	800 + 209	Cortex A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	0	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP153AAD1	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153AAB1	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153AAC1	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153AAA1	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	[Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153CAD1	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 Quad	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP153CAB1	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP153CAC1	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP153CAA1	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	STM32MP153系列- 双核Arm Cortex-A7 + Cortex-M4 + 3D GPU + DSI + CAN FD																														STM32MP157系列- 双核Arm Cortex-A7 + Cortex-M4 + 3D GPU + DSI + CAN FD																						
	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IONb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit channels	Nb ADC 16-bit Cell	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	USART	LPUART	CAN	SDIO	FSMC	FMC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSIHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC	
STM32MP153DAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153DAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153DAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153DAA1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2[HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	0	1
STM32MP153FAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP153FAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP153FAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP153FAA1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	0	0	4	1	8	1	0	0	1	0	0	1	1
STM32MP157AAD3	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1
STM32MP157AAB3	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1
STM32MP157AAC3	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1
STM32MP157AAA3	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1

STM32 MP1系列 – 双核Arm® Cortex®-A7和Arm® Cortex®-M4高超性能MPU

Commercial Product Code	Frequency (MHz)	Core	Flash (Kbytes)	Ram (Kbytes)	E2prom (Bytes)	Package Name	IO Nb	Supply voltage	Nb Timer (16-bits)	Nb Timer (32-bit)	Nb motor control Timer (16bits)	Nb Low Power Timer	Nb High Resolution Timer	Nb ADC 12-bit Cell	Nb ADC 12-bit channels	Nb ADC 16-bit Cell	Nb ADC 16-bit channels	Nb DAC 12-bit channels	COMP	OPAMP	Cordic	FMAC	SPI	QuadSPI/OctoSPI	I2S	I2C	U(S)ART	LPUART	CAN	SDIO	FSMC	FMCC	USB Device	USB OTG_FS	USB OTG_HS	Ethernet	MDIOS	Segment LCD	TFT LCD	JPEG Codec	3D GPU	DSHOST	SAI	SPDIFRX	DFSDM	DCMI	SWPMI	RF	TRNG	OTFDEC	PKA	AES/DES	SHA/HMAC
STM32MP157CAD3	650 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1
STM32MP157CAB3	650 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1
STM32MP157CAC3	650 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1
STM32MP157CAA3	650 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1
STM32MP157DAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1
STM32MP157DAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1
STM32MP157DAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1
STM32MP157DAI1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	0	1
STM32MP157FAD1	800 + 209	Dual A7 + M4	0	708	0	TFBGA257	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1
STM32MP157FAB1	800 + 209	Dual A7 + M4	0	708	0	LFBGA354	98	1.7-3.6	12	2	2	5	0	0	0	2	17	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1
STM32MP157FAC1	800 + 209	Dual A7 + M4	0	708	0	TFBGA361	148	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1
STM32MP157FAA1	800 + 209	Dual A7 + M4	0	708	0	LFBGA448	176	1.7-3.6	12	2	2	5	0	0	0	2	22	2	0	0	0	0	6	2 [Quad]	[3]	6	4+4	0	2 [FD]	3	0	1	0	1	2 [HS HOST]	1 [Giga]	1	0	1	0	1	1	4	1	8	1	0	0	1	0	0	1	1

缩写和封装

缩写

ADC : Analog-to-digital converter
ART : Auto-reload timer
ATAPI : AT attachment packet interface
AWU : Auto wake-up from halt
BLPD : Byte level protocol decoder
BOD : Brown-out detector
CAN : Controller area network
CAPCOM : Capture compare
CSS : Clock security system
DALI : Digital addressable lighting interface
DDC : Data display channel
DiSEqC : Digital satellite equipment control
DMA : Direct memory access
DSC : Dual supply control
DTC : Data transfer coprocessor
ETM : Embedded trace macrocell
EMI : External memory interface
HDLC : High-level data link control
IAP : In-application programming
IC/OC : Input capture/output compare
ICP : programming
IR : Infrared
IrDA : Infrared data association
ISP : In-situ programming
I²C : Inter-integrated circuit
I²S : Inter-IC sound

LCD : Liquid crystal display
LIN : Local interconnect network
LVD : Low voltage detection
MAC : Multiply accumulator
MC : Motor control
MFT : Multifunction timer
MMC : MultiMediaCard
NMI : Non-maskable interrupt
OSG : Oscillator safeguard
PCA : Programmable counter array
PDR : Power-down reset
PHW : Programmable halt wake-up
PEC : Peripheral event controller
PLD : Programmable logic device
PLL : Phase locked loop
POR : Power-on reset
PVD : Programmable voltage detector
PVR : Programmable voltage regulator
PWM : Pulse width modulation
ROP : Readout protection
RTC : Real-time clock timer
SAI : Serial Audio Interface
SC : Smartcard
SCI : Serial communication interface
SCR : Smartcard reader
SDIO : Secure digital input output
SDMMC : Secure Digital / Multi Media Card
SMI : Serial memory interface

SPI : Serial peripheral interface
SSC : Single-cycle switching support
SSP : Synchronous serial port
TBU : Time base unit
TLI : Top level interrupt
UART : Universal asynchronous receiver
transmitter
USART : Universal sync/async receiver
transmitter
USB : Universal Serial Bus
WDG : Watchdog timer
WWDG : Window watchdog timer

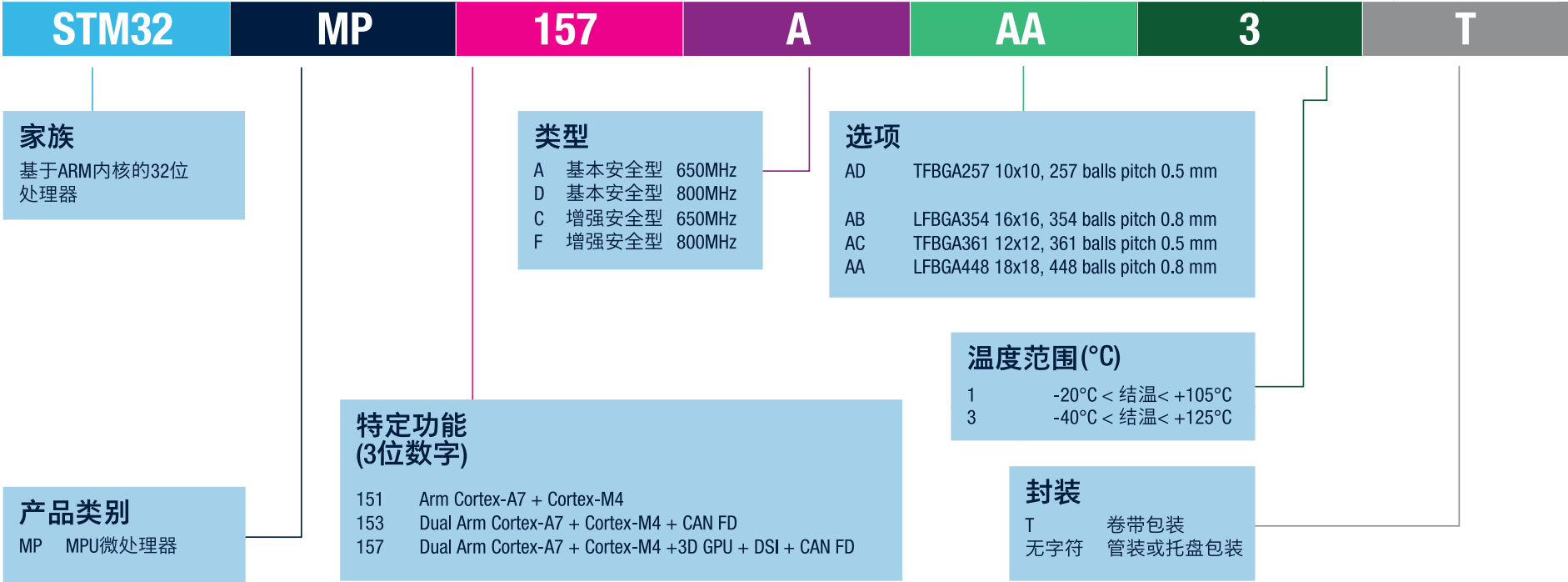
封装

DIP : Dual in-line package
LCC : Leaded chip carrier
PDIP Shrink : Shrink Plastic Dual In-line
Package
PQFP : Plastic quad flat package
SO : Small outline
LQFP : Low-profile quad flat package
PBGA : Plastic ball grid array
DFN : Dual flat no-lead
QFN : Quad flat no-lead
WLCSP : Wafer-Level Chip-Scale Package

STM32 & STM8产品型号(仅适用于MCU)



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